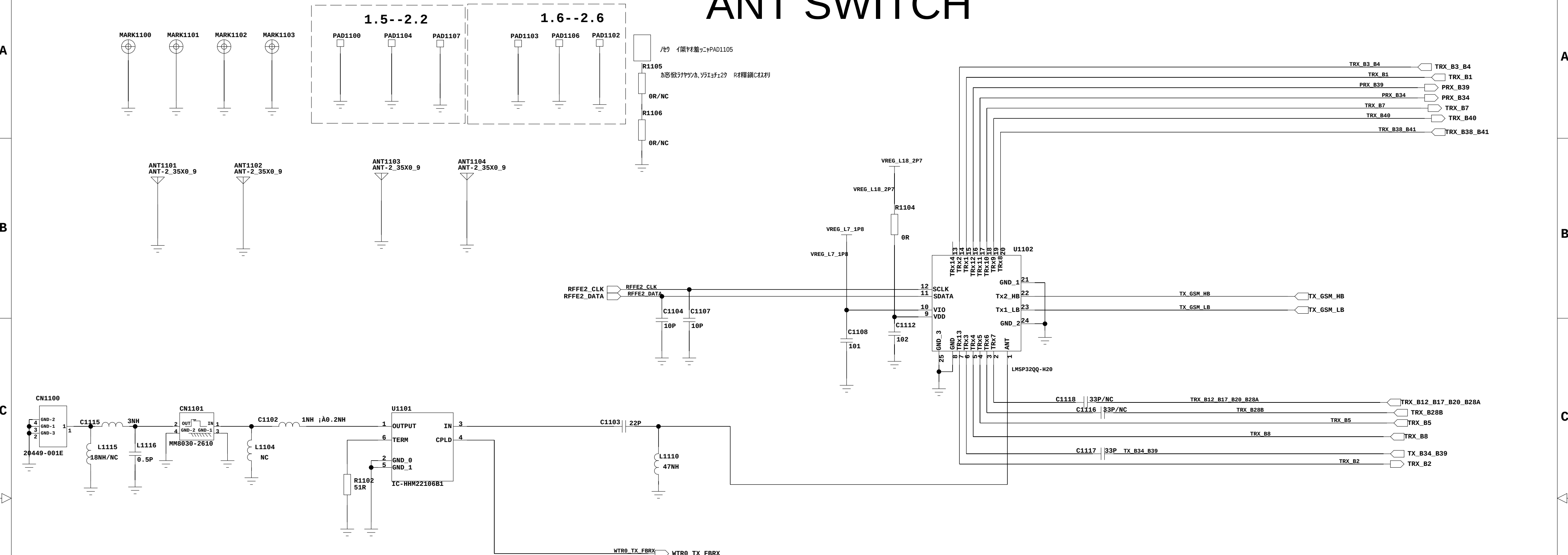
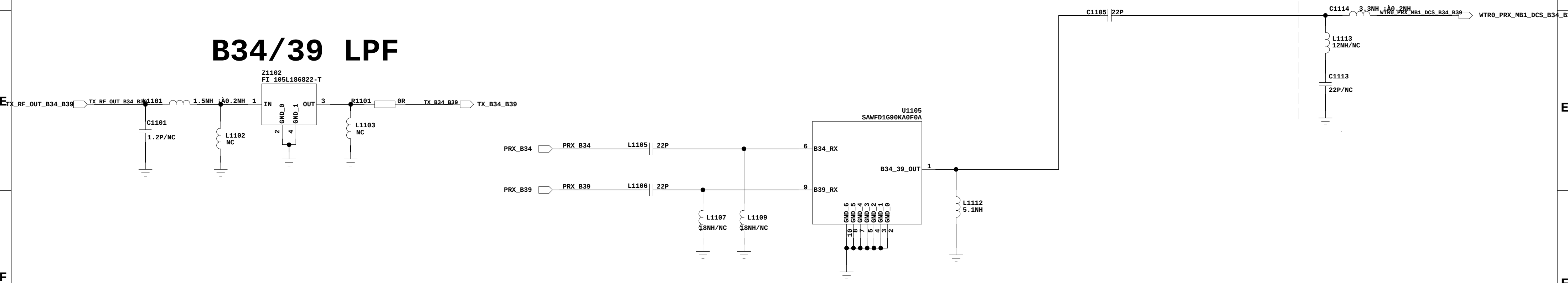


ANT SWITCH



B34/39 LPF

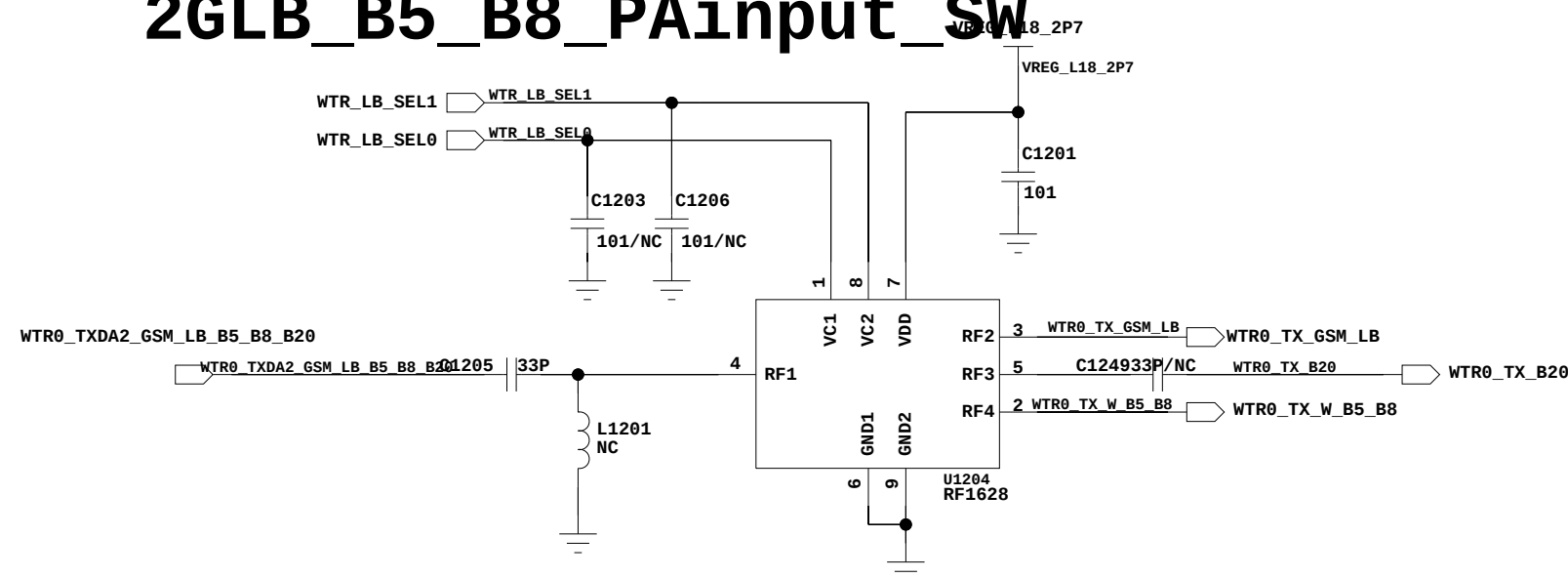


B34/39 RX SAW

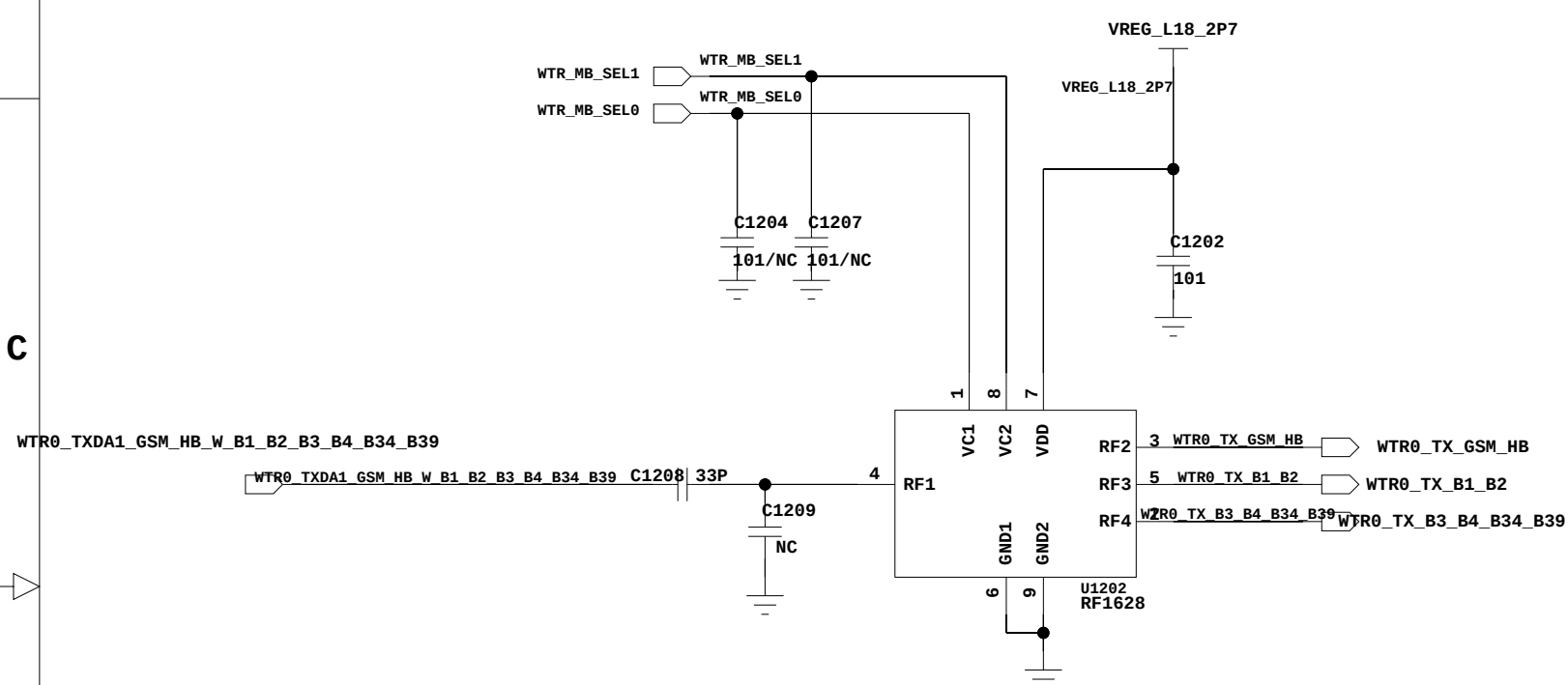
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Drawn By			Draw No: 15011
Checked By			Sheet 1_ASM of 30 Ver:
Standard By			
Approved By			

oppo

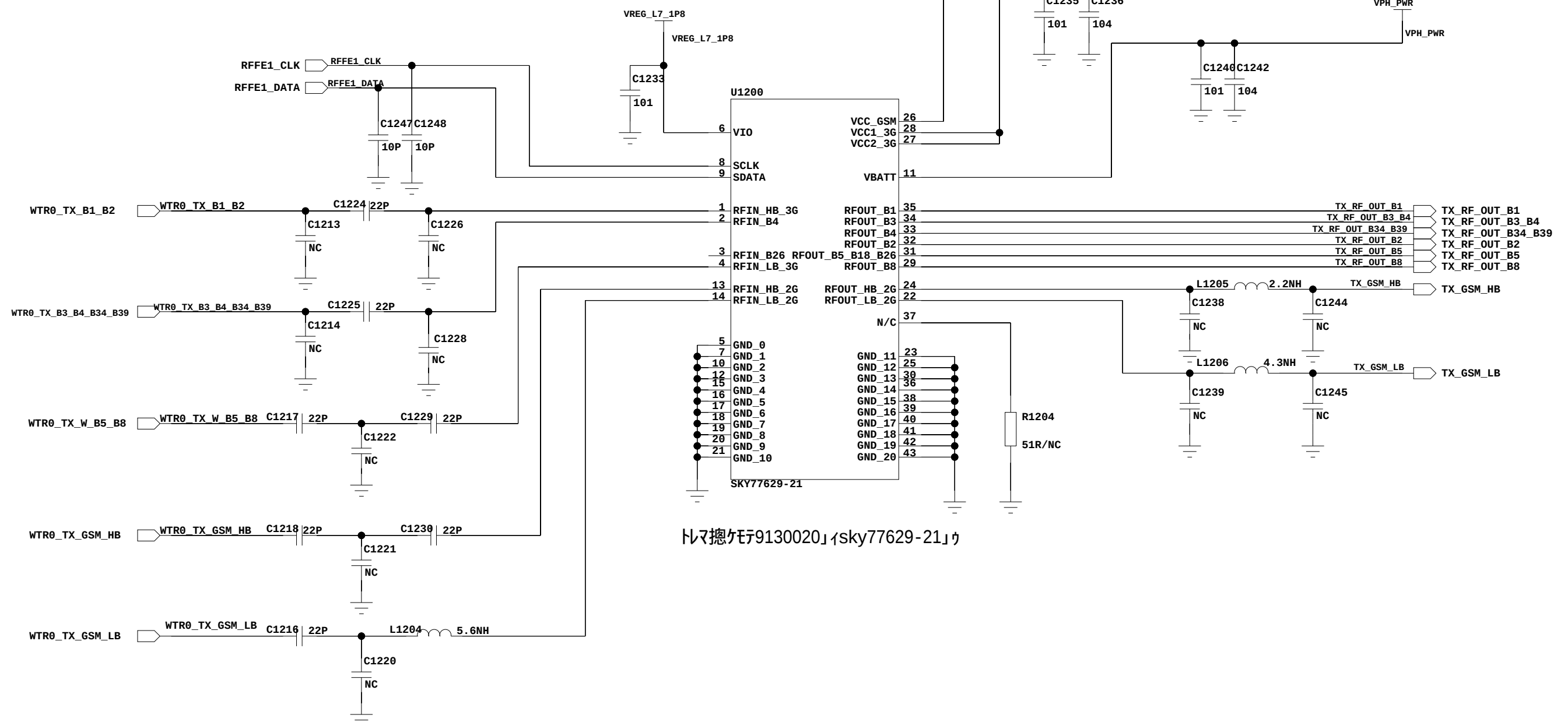
2GLB_B5_B8_PAinput_SW



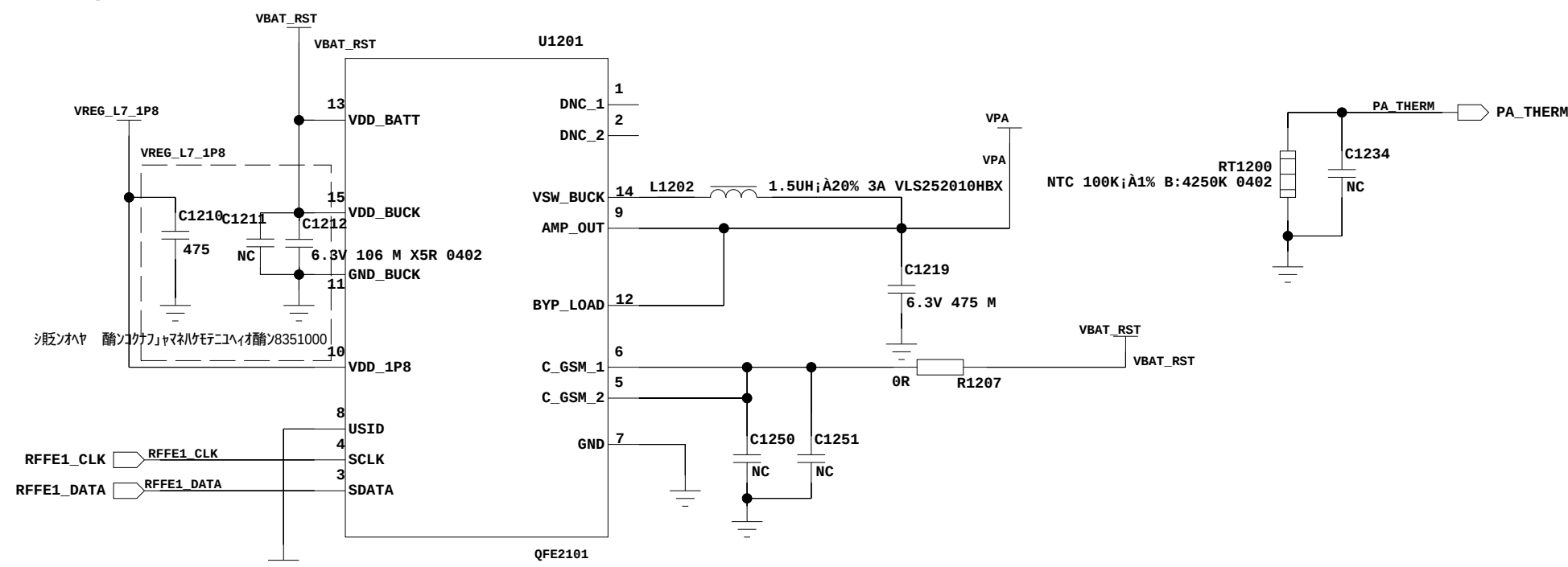
2GHB_B1_B2_B3_B4_PAinput_SW



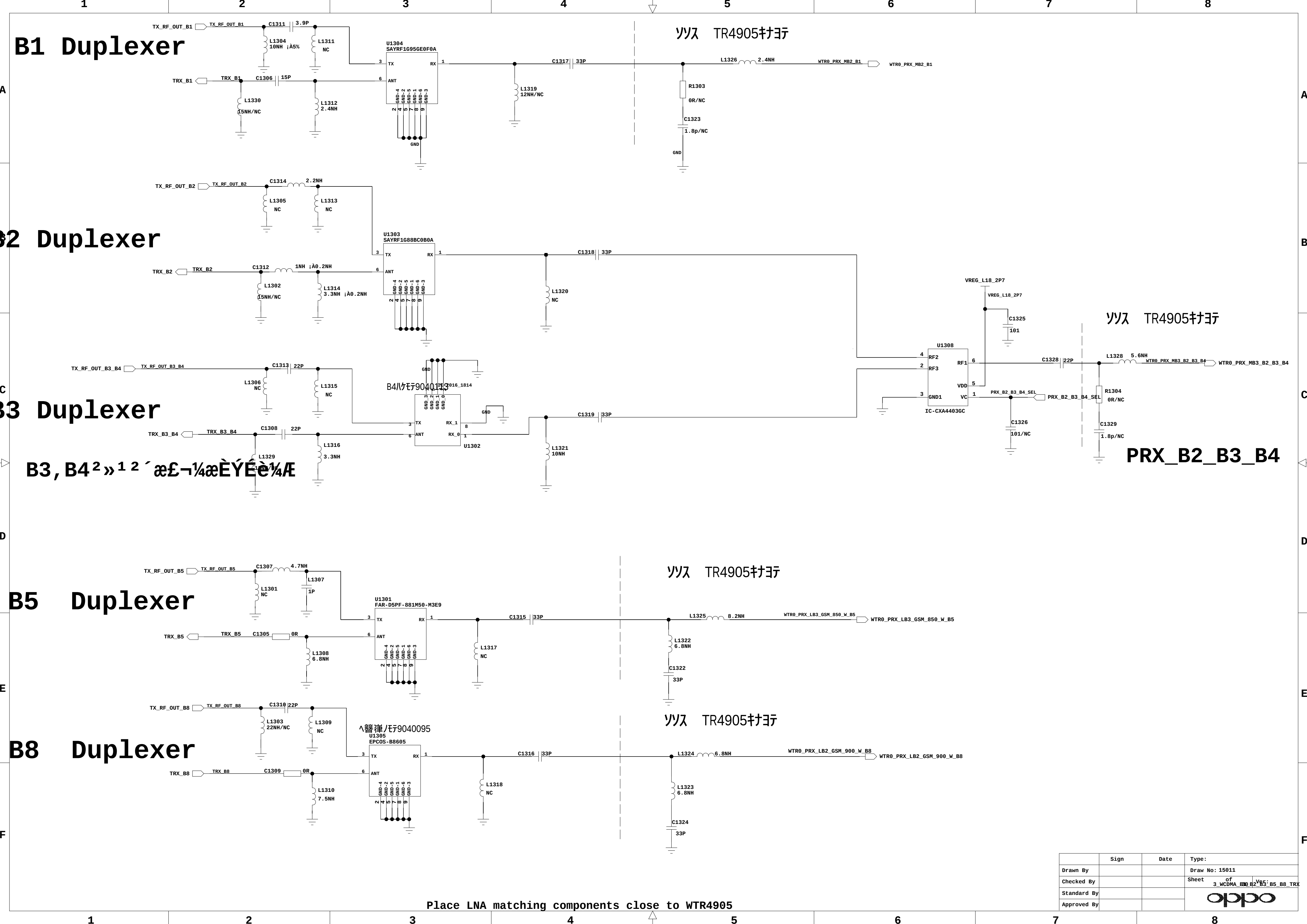
MMMB PA



QFE2101



	Sign	Date	Type:	
Drawn By			Draw No: 15011	
Checked By			Sheet of 2 MMPA QFE2109	Ver:
Standard By				
Approved By				



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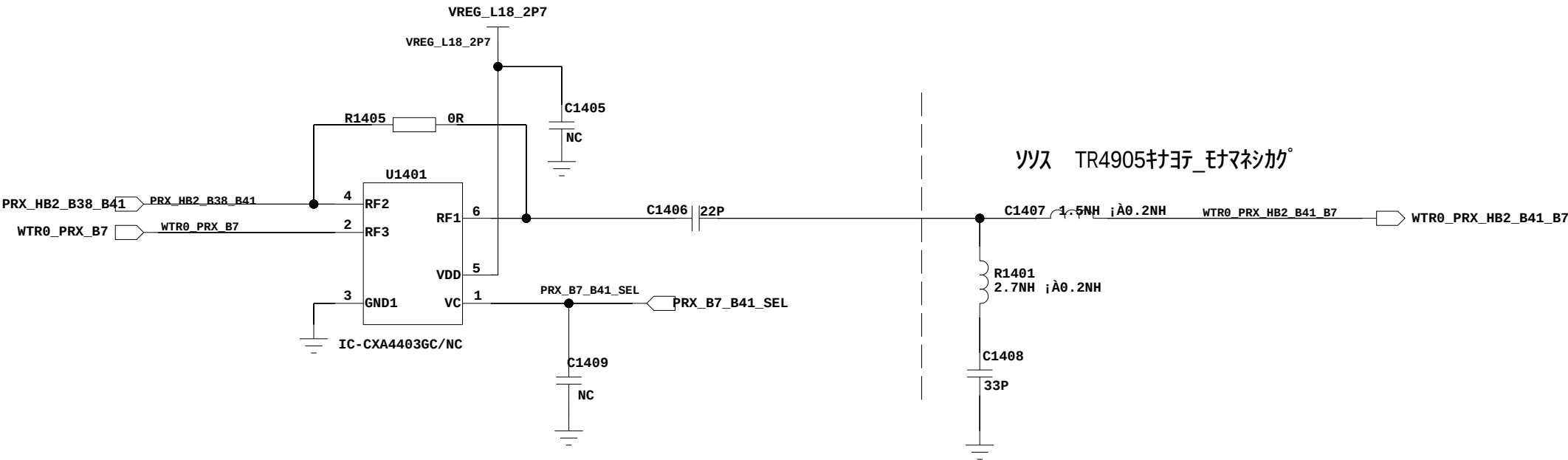
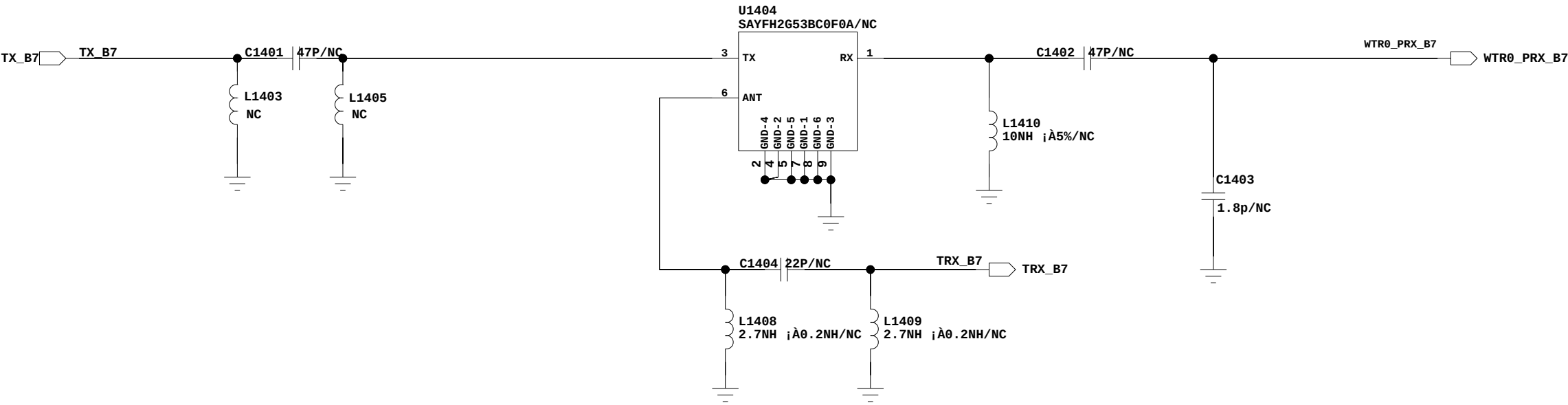
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E

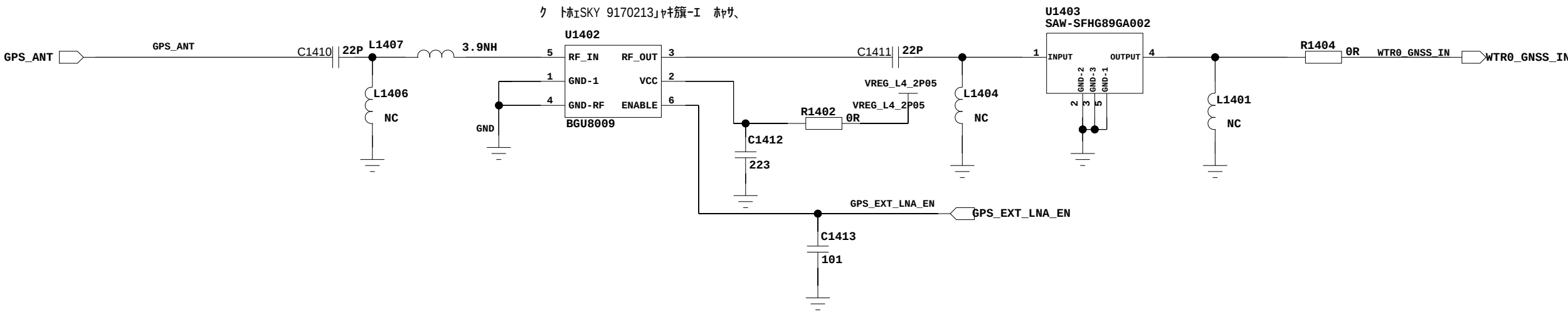
F

B7 TRX

PRX B7 & B41 Switch



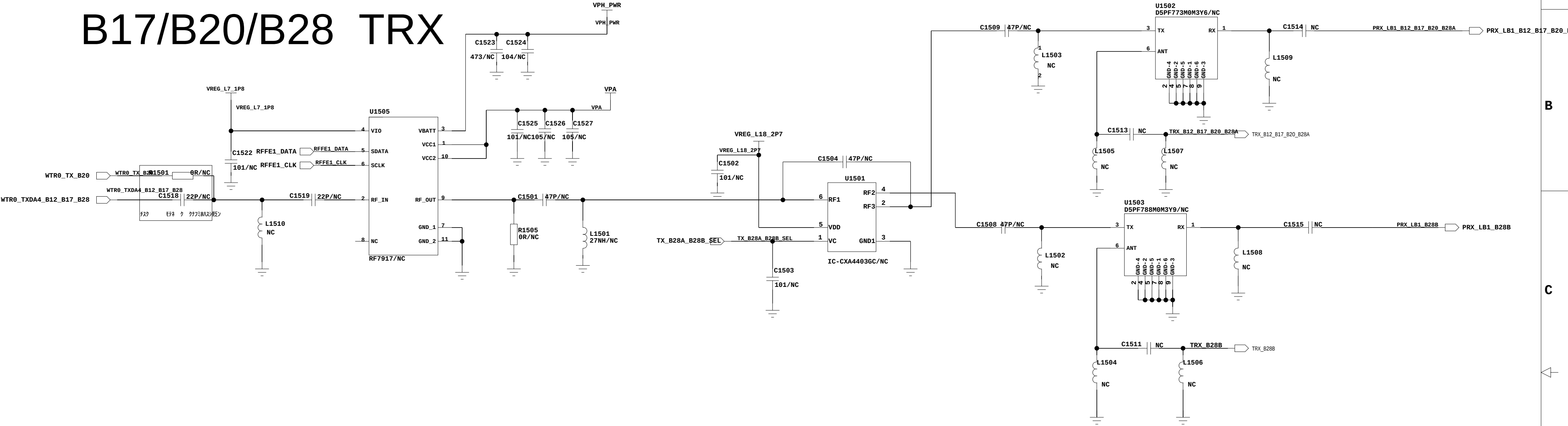
GPS 0323-2023 By Hanyun



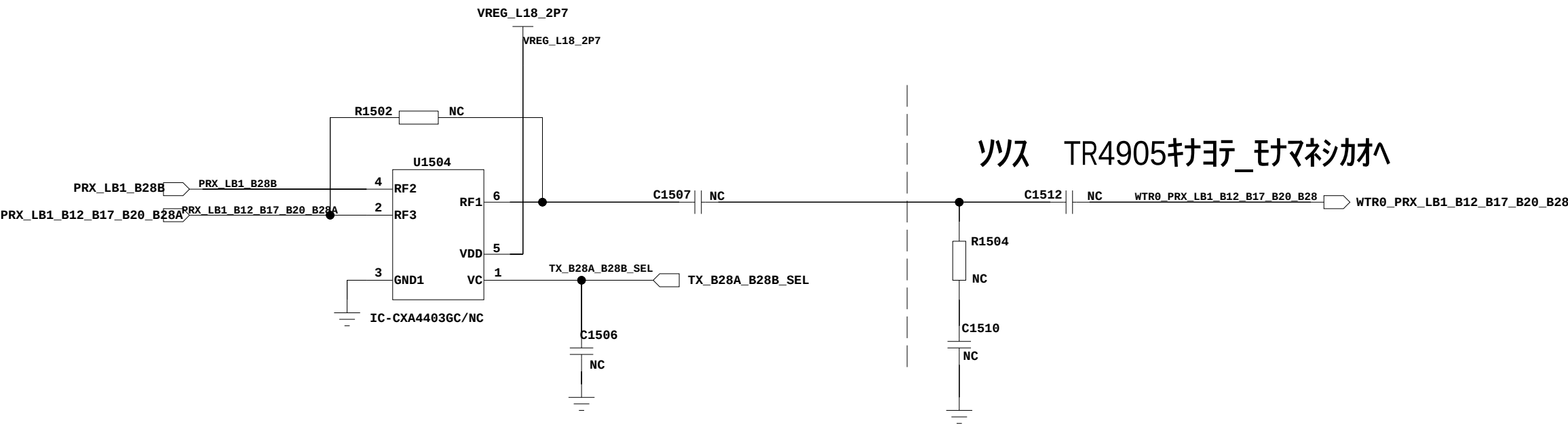
	Sign	Date	Type:
Drawn By			Draw No: 14046
Checked By			Sheet 1 of 29 Ver:
Standard By			
Approved By			



B17/B20/B28 TRX



B28A and B28B PRX SW



リッス TR4905村ヨリ_母マネシカハ

	Sign	Date	Type:
Drawn By			Draw No: 15011
Checked By			Sheet 5 of 5
Standard By			Approved By
Approved By			



1

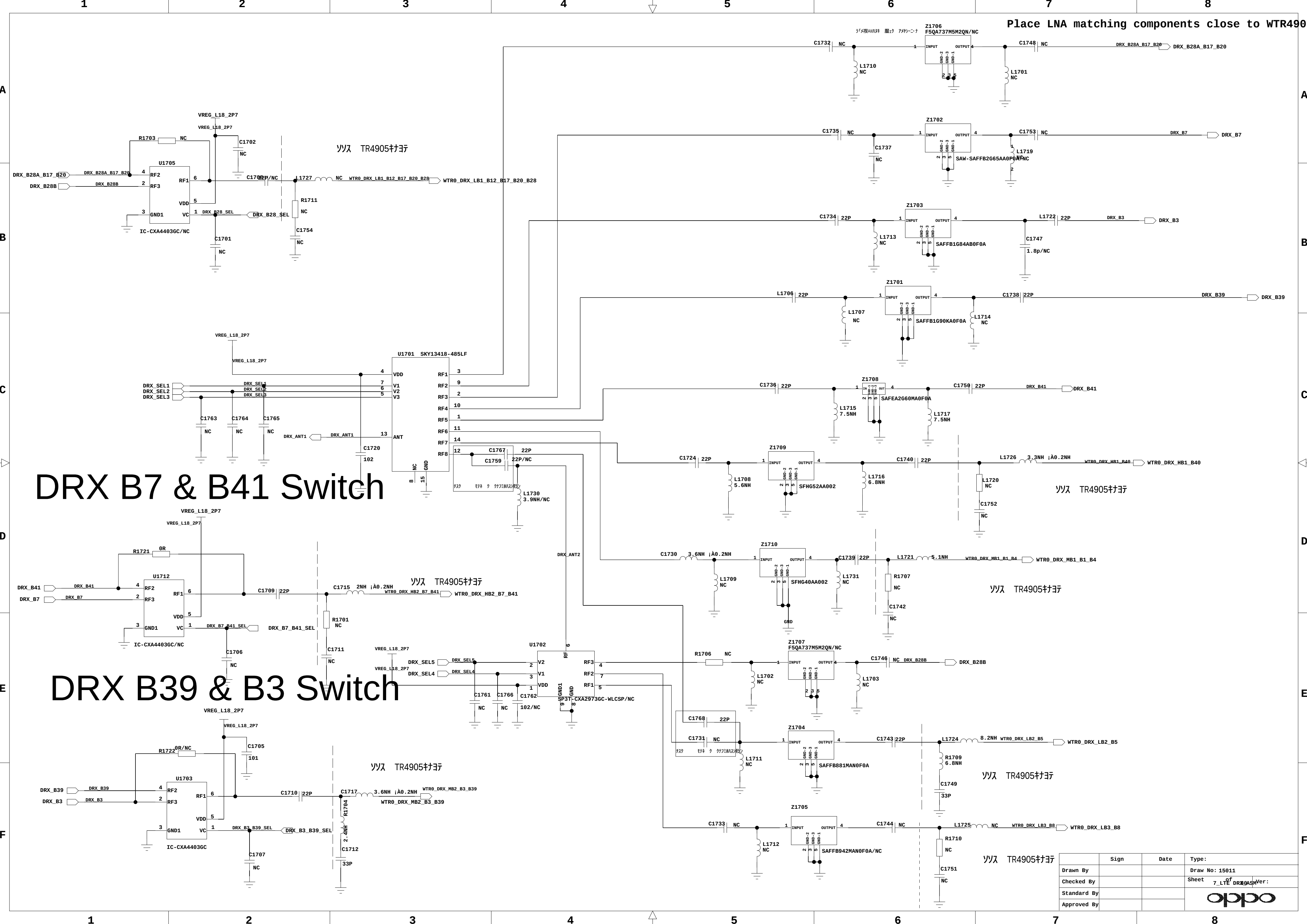
34

5

6

7

8



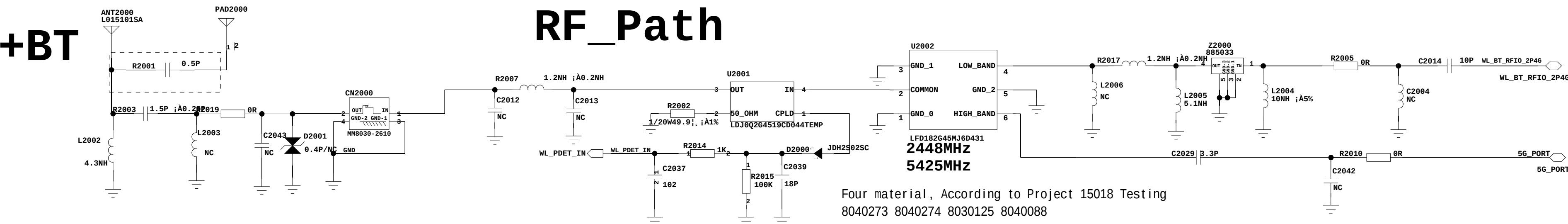
DRX B7 & B41 Switch

DRX B39 & B3 Switch

	Sign	Date	Type:
Drawn By			Draw No: 15811
Checked By			Sheet 7 of 7
Standard By			DRX B7 & B41 Switch
Approved By			
oppo			

WIFI+BT

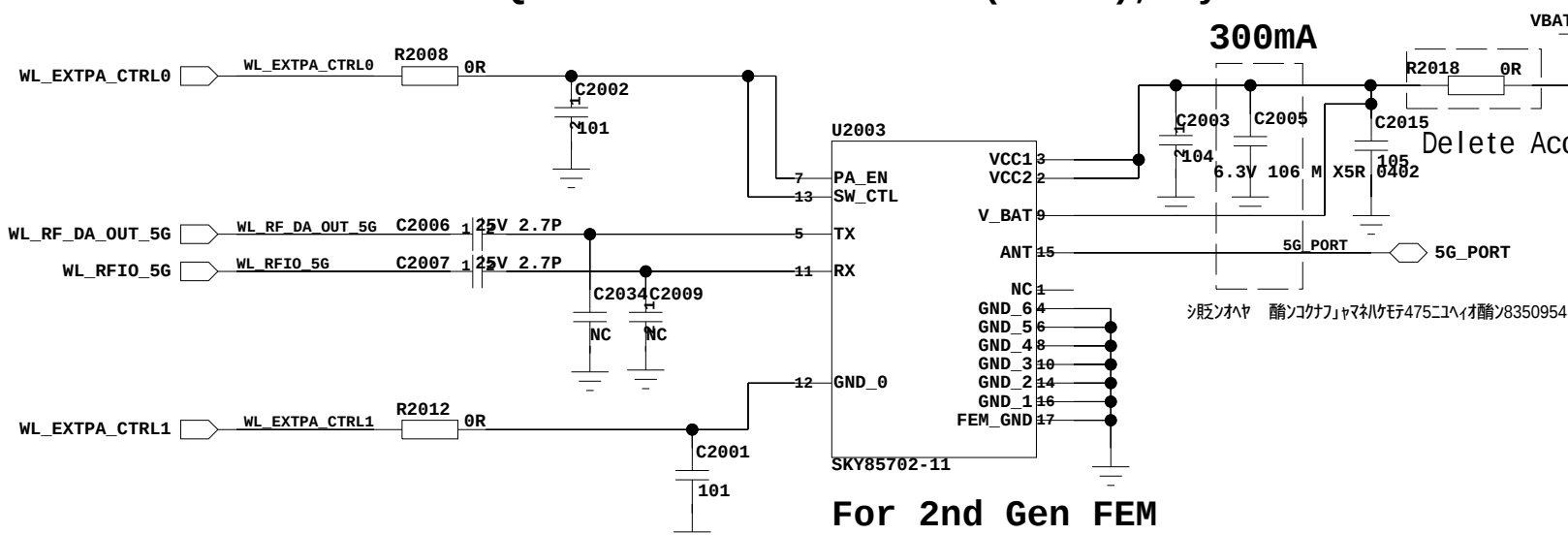
RF_Path



Qualcomm recommend 475(C2015),Pay attention while debugging

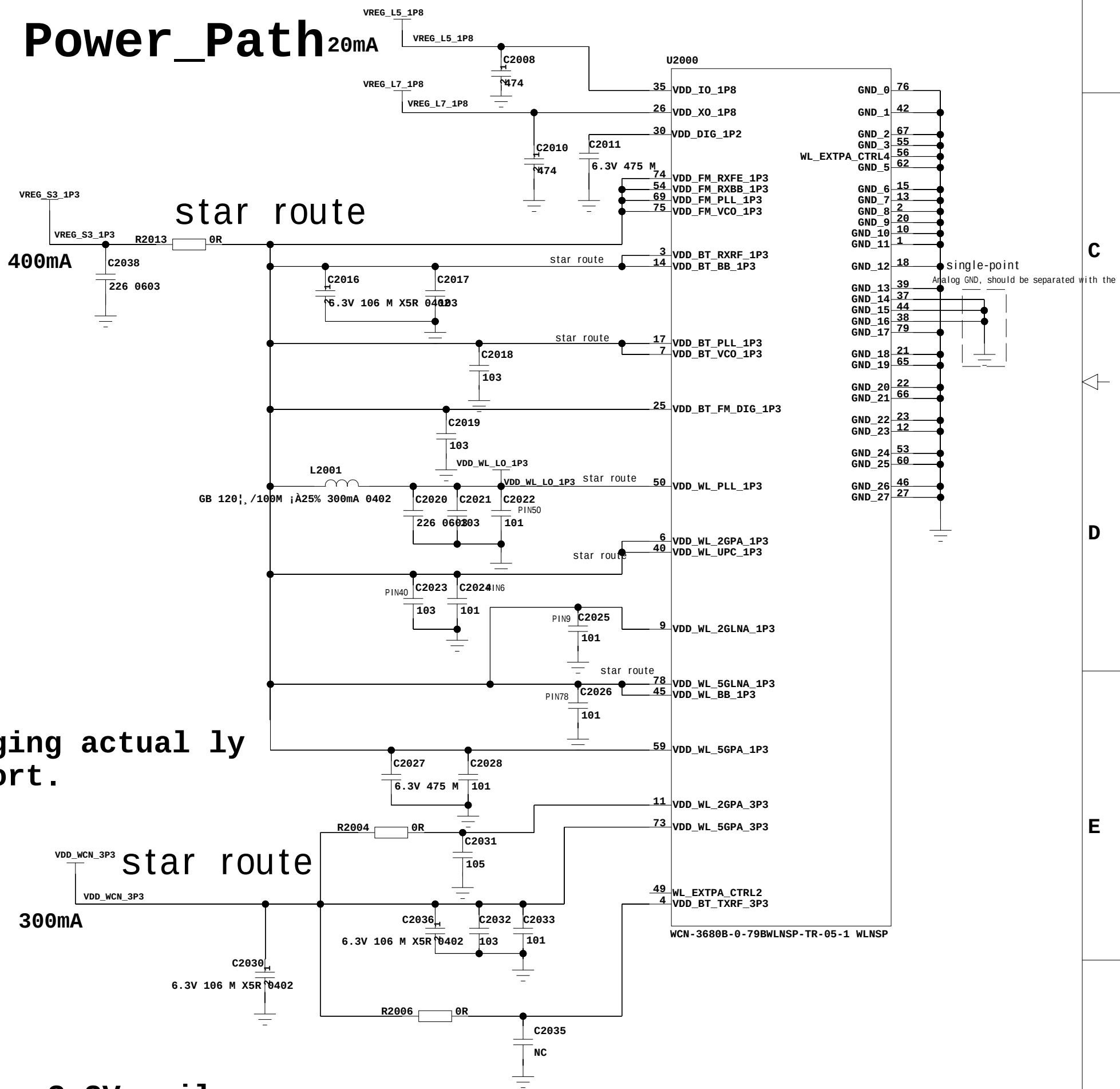
FEM

WCN3660B, and WCN3680B are P2P, and IC ident

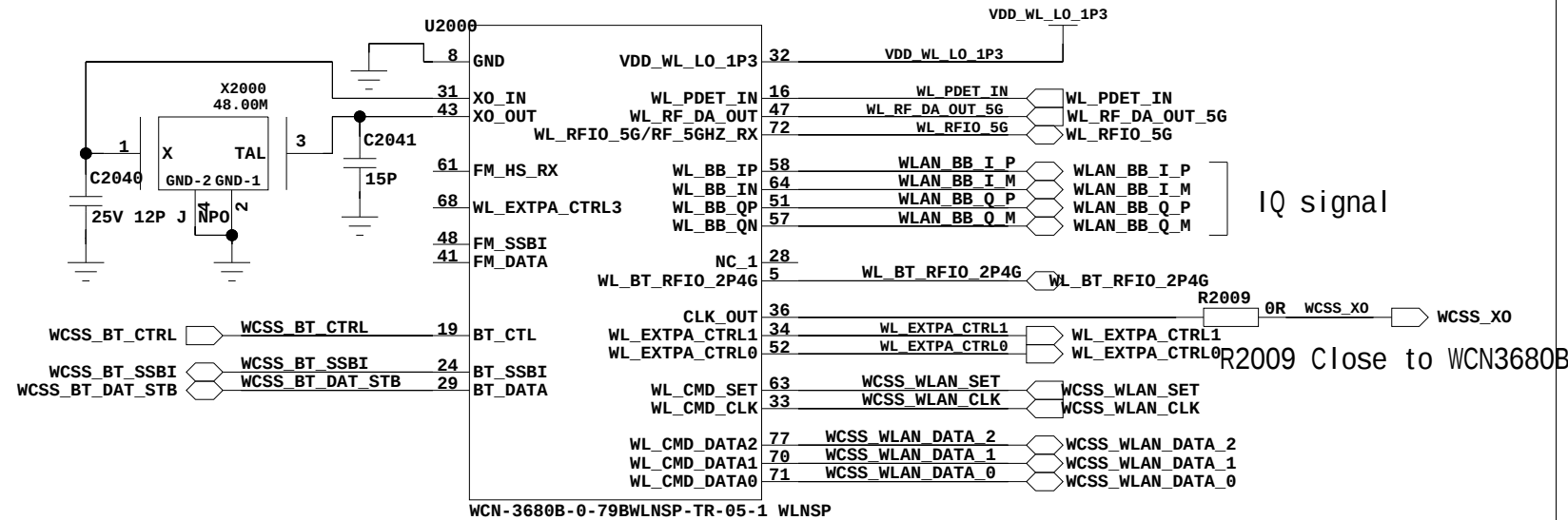


Power_Path

star route



WIFI_HOST_PORT/TCX0



Two filter capacitors placed on the sides of 48 MHZ crystal C2041 should be bigger than C2040 to make sure TCX0 ramp up

Power_Path_Width****

Power Trace name	Trace width
VREG_L19_2P9 (3.3 V)	0.3 mm
VREG_L11_1P25 (1.3 V)	0.4 mm
VBATT to 5G PA	0.3 mm
VDD_WL_2GPA_2P9	0.25 mm
VDD_WL_5GPA_1P3	0.2 mm
VDD_WL_2GPA_1P3	0.2 mm
VDD_BT_TXRF_2P9	0.15 mm

MSM8939 carry WCN3680B or WCN3660B

FM power rails must remain connected, but decoupling caps may be removed.

L2001: Qualcomm use BLM03AX241SN1B

2400hm@100MHz 0.35A DCR<0.380hm

For "B" version devices do not connect pin 49 to the 3.3V rail

Refer: 80-WL005-43 WCN3660-A-B, WCN3680-B REFERENCE SCHEMATIC

All power 0, should be placed close to the Star nodes

Applefix.vn

If 2.4 GHz WLAN spurs are observed around 2.6 to 2.7 GHz, replace R2006 with 20 nH inductor.

	Sign	Date	Type:
Drawn By			Draw No: 15011
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Standard By			
Approved By			

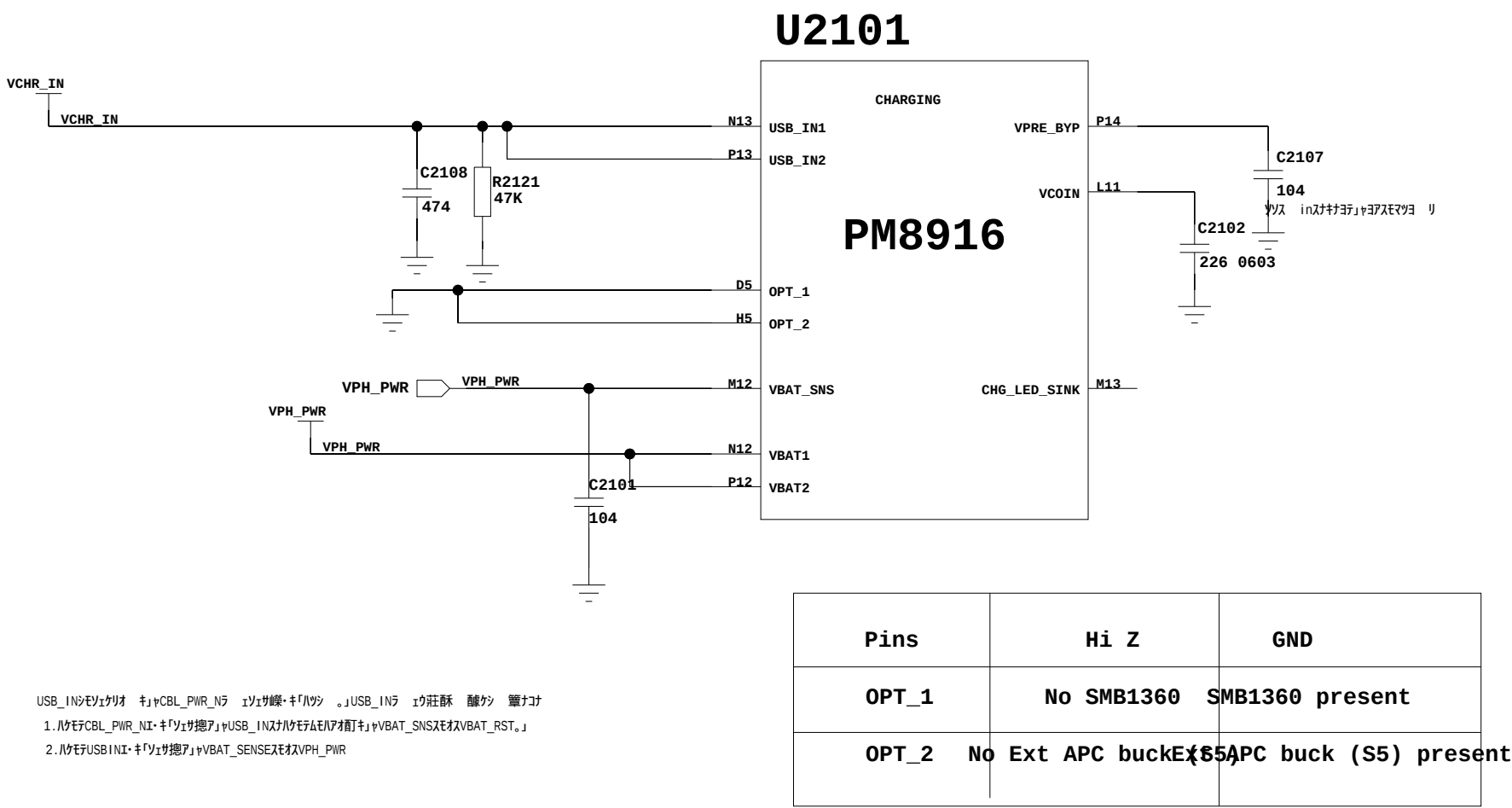
oppo

PM8916 CHARGING

A

B

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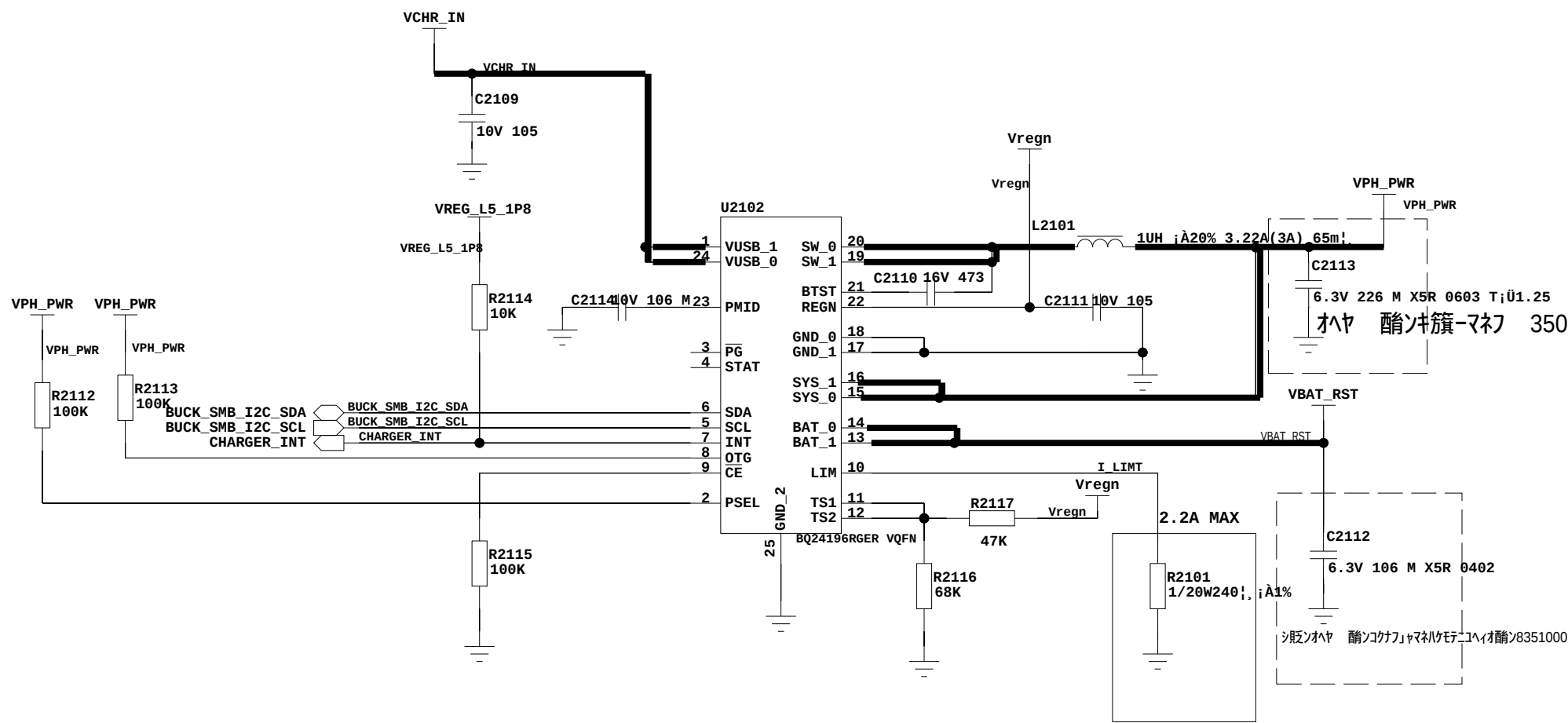


Circuit of Ext. Charge

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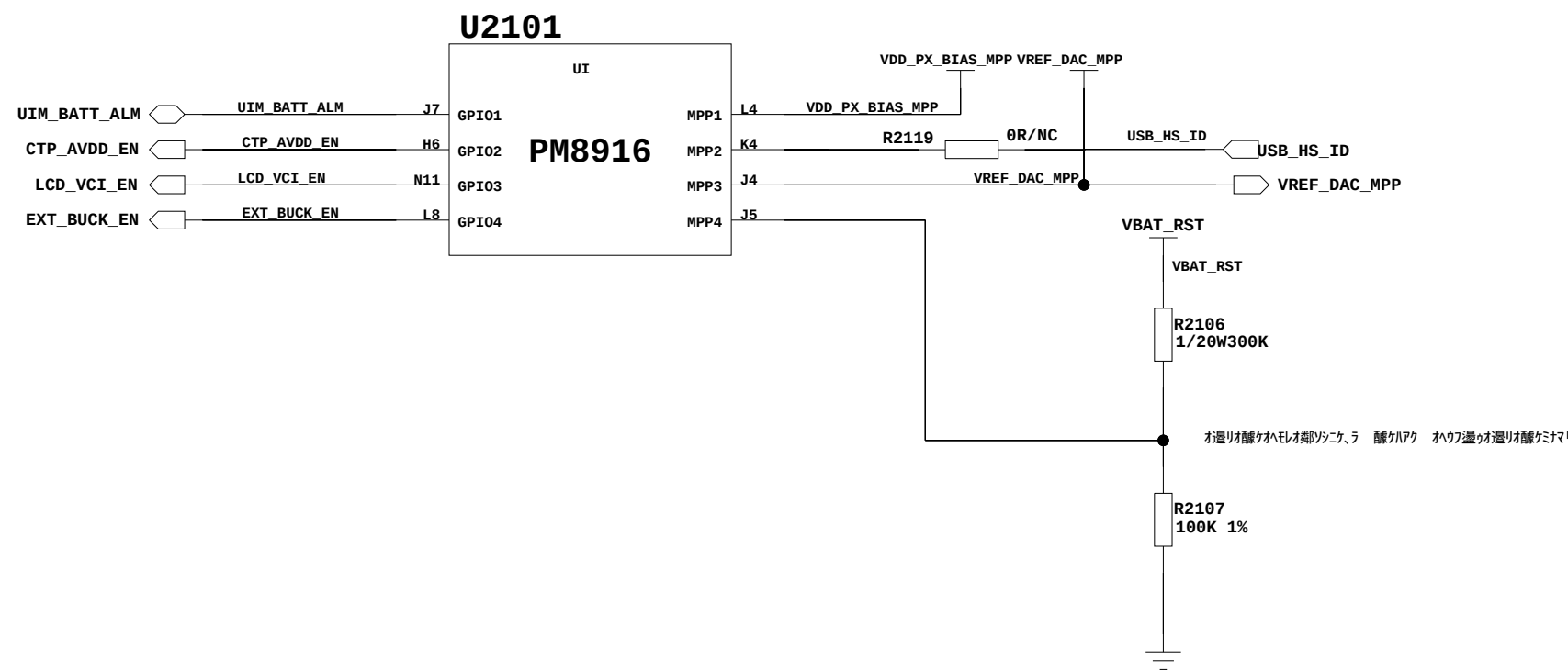


GPIO&MPP

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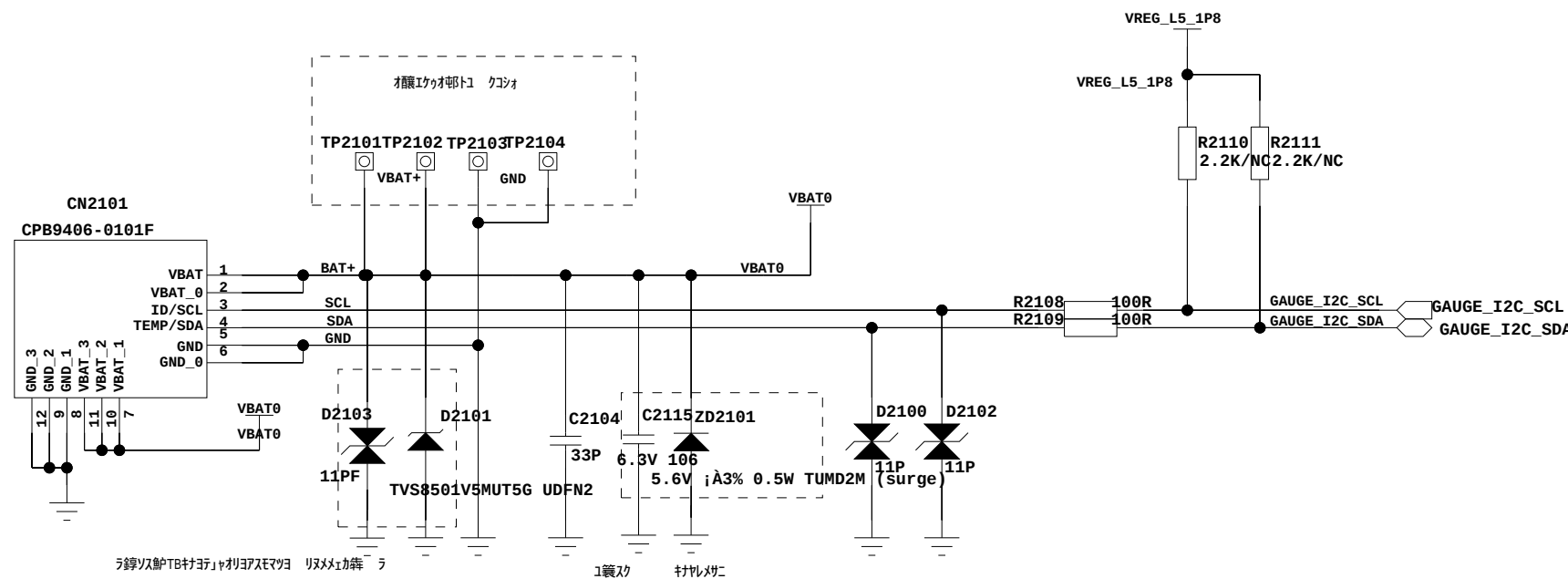


Battery Connector

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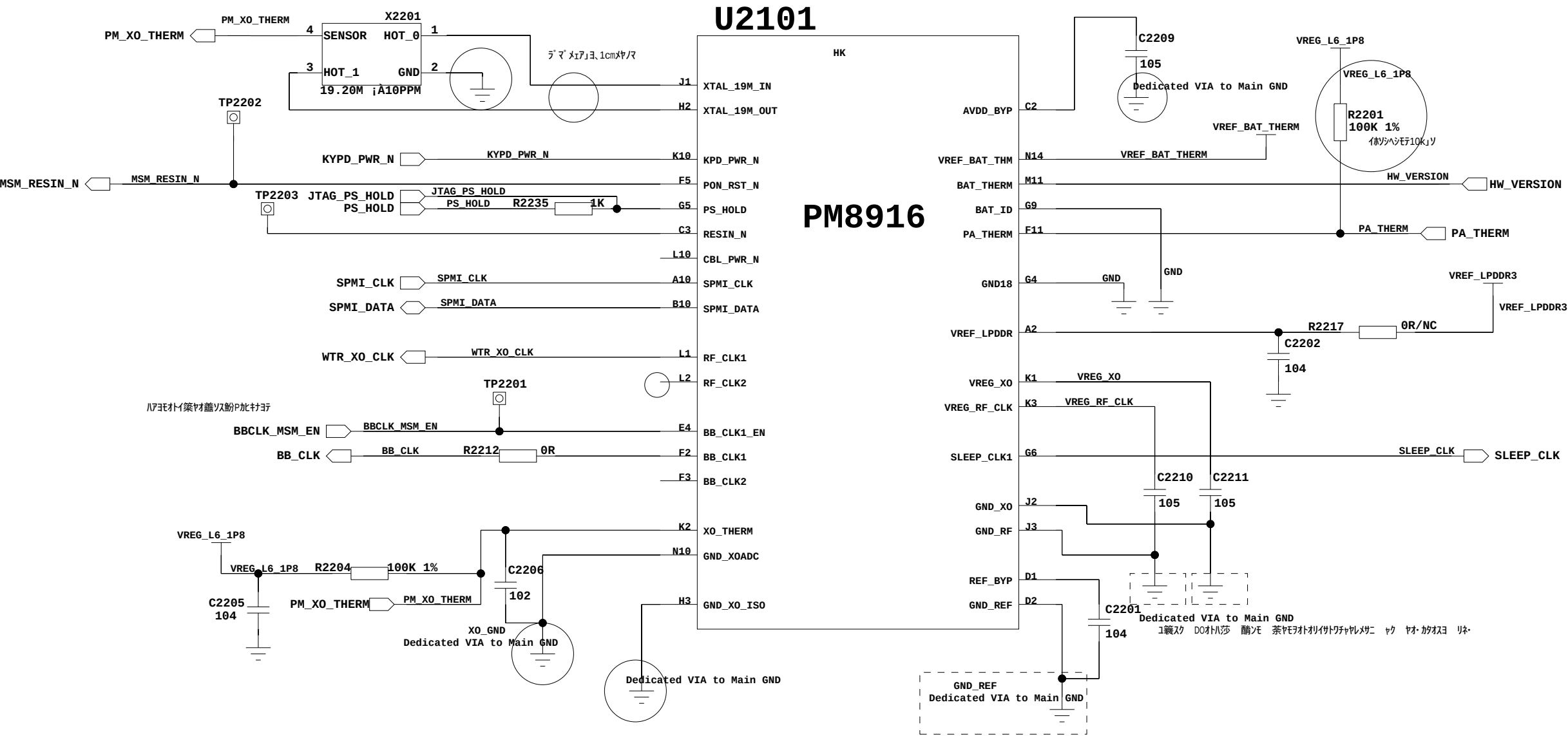
F



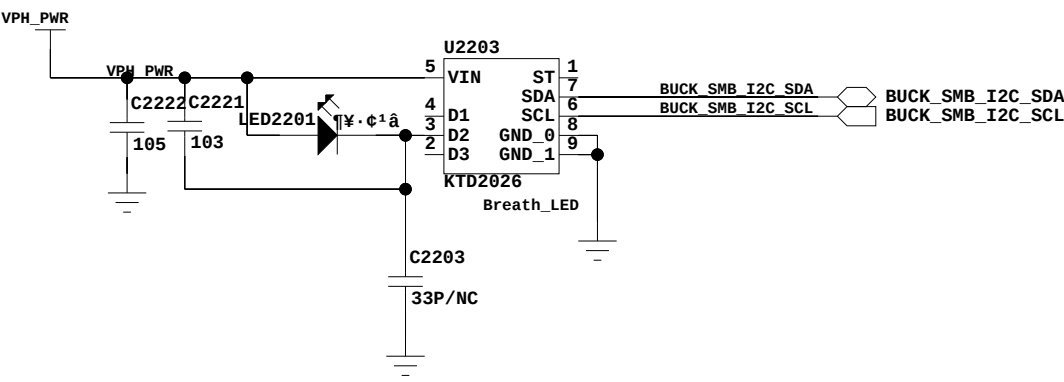
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Approved By			



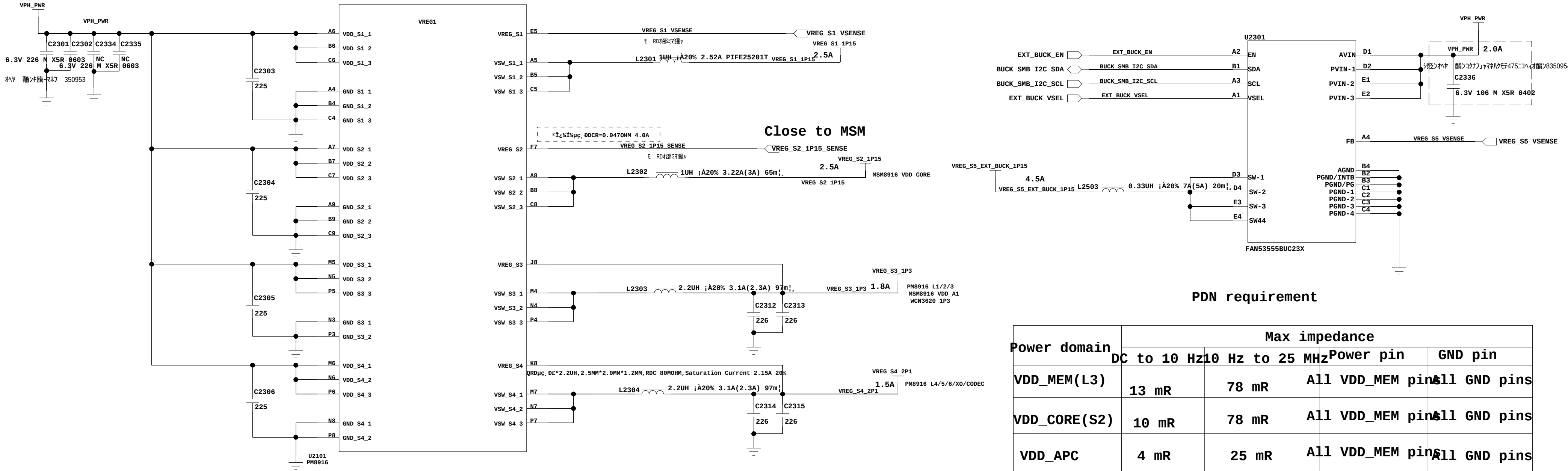
CLK_CTRL



RGB LED DRIVER



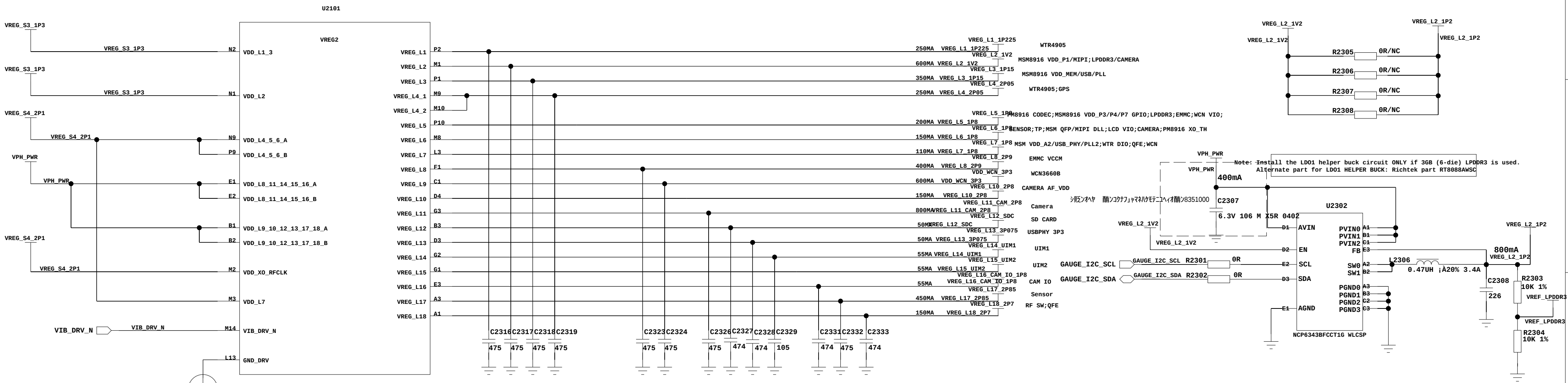
SMPS



PDN requirement

Power domain	Max impedance			
	DC to 10 Hz	10 Hz to 25 MHz	Power pin	GND pin
VDD_MEM(L3)	13 mR	78 mR	All VDD_MEM pins	All GND pins
VDD_CORE(S2)	10 mR	78 mR	All VDD_MEM pins	All GND pins
VDD_APC	4 mR	25 mR	All VDD_MEM pins	All GND pins
VDD_MODEM(S1)	22 mR	78 mR	M19, P19K17,	K21, T19, T17, P17, P21
	22 mR	78 mR	AK25, AK27, AM27, AP25, AT25	AT27, AM23, AM29, AK23, AK29
VDD_P1(L2)	TBD	TBD	All VDD_P1 pins	All GND pins

LD0s



	Sign	Date	Type:
Drawn By			Draw No: 15811
Checked By			Sheet of 30 Ver:
Standard By			oppo
Approved By			

Codec

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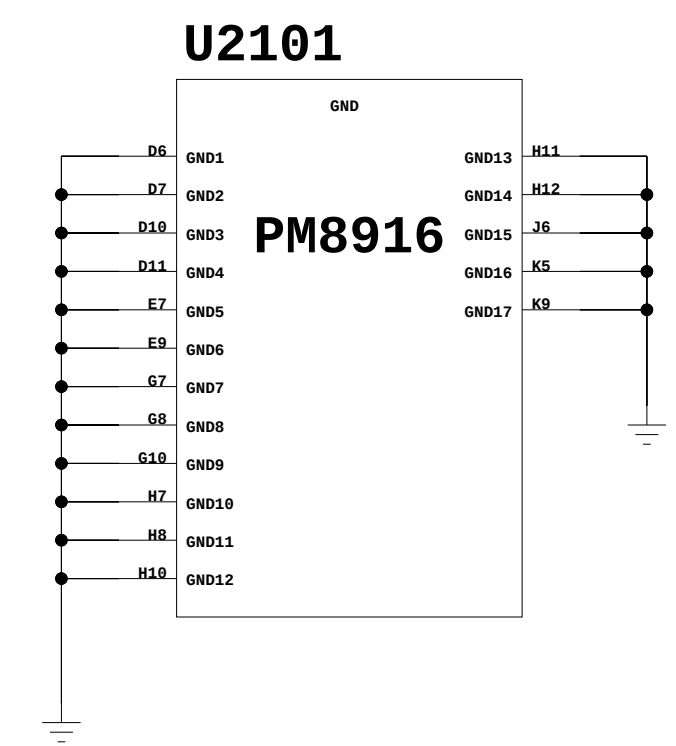
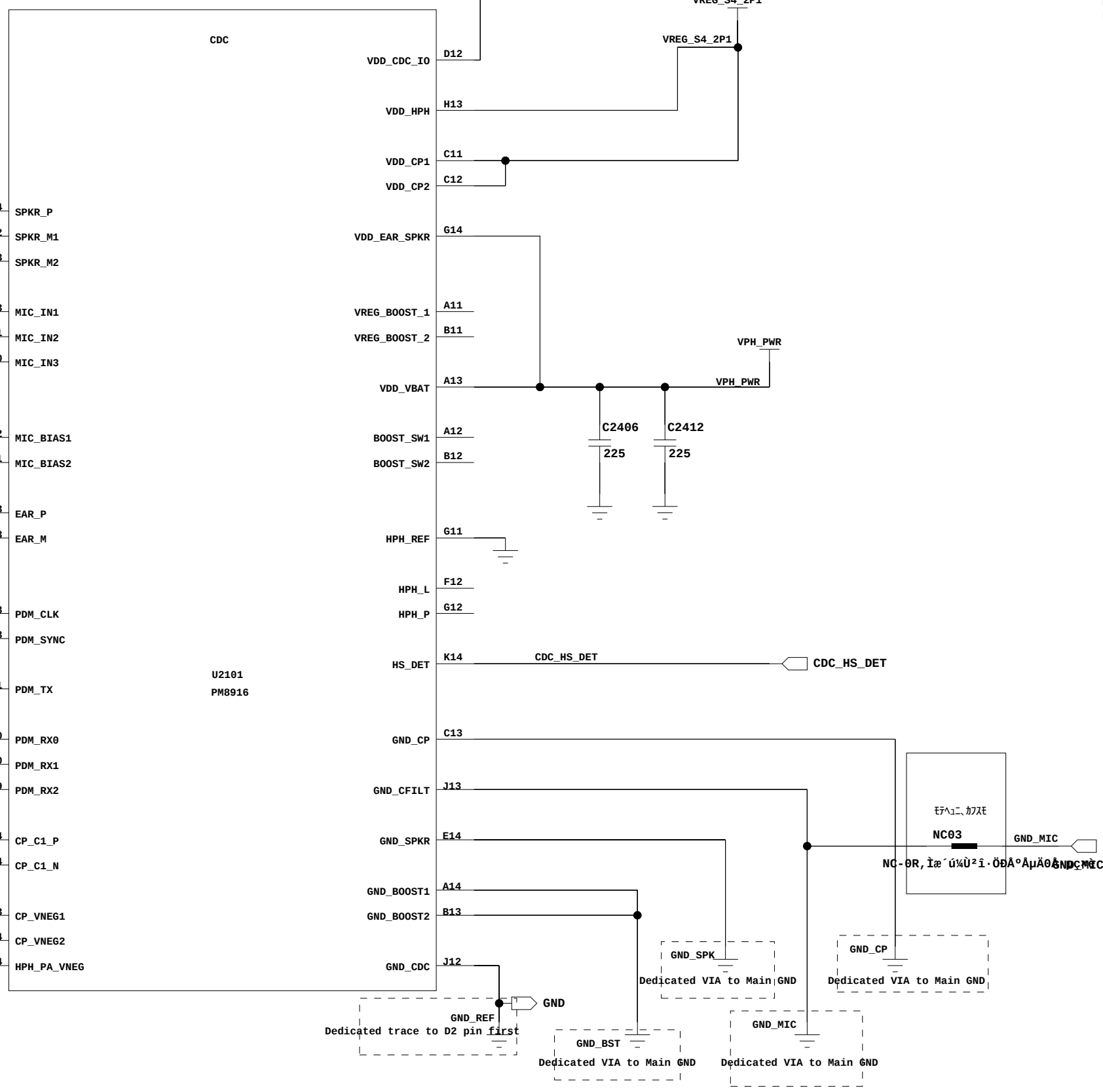
B

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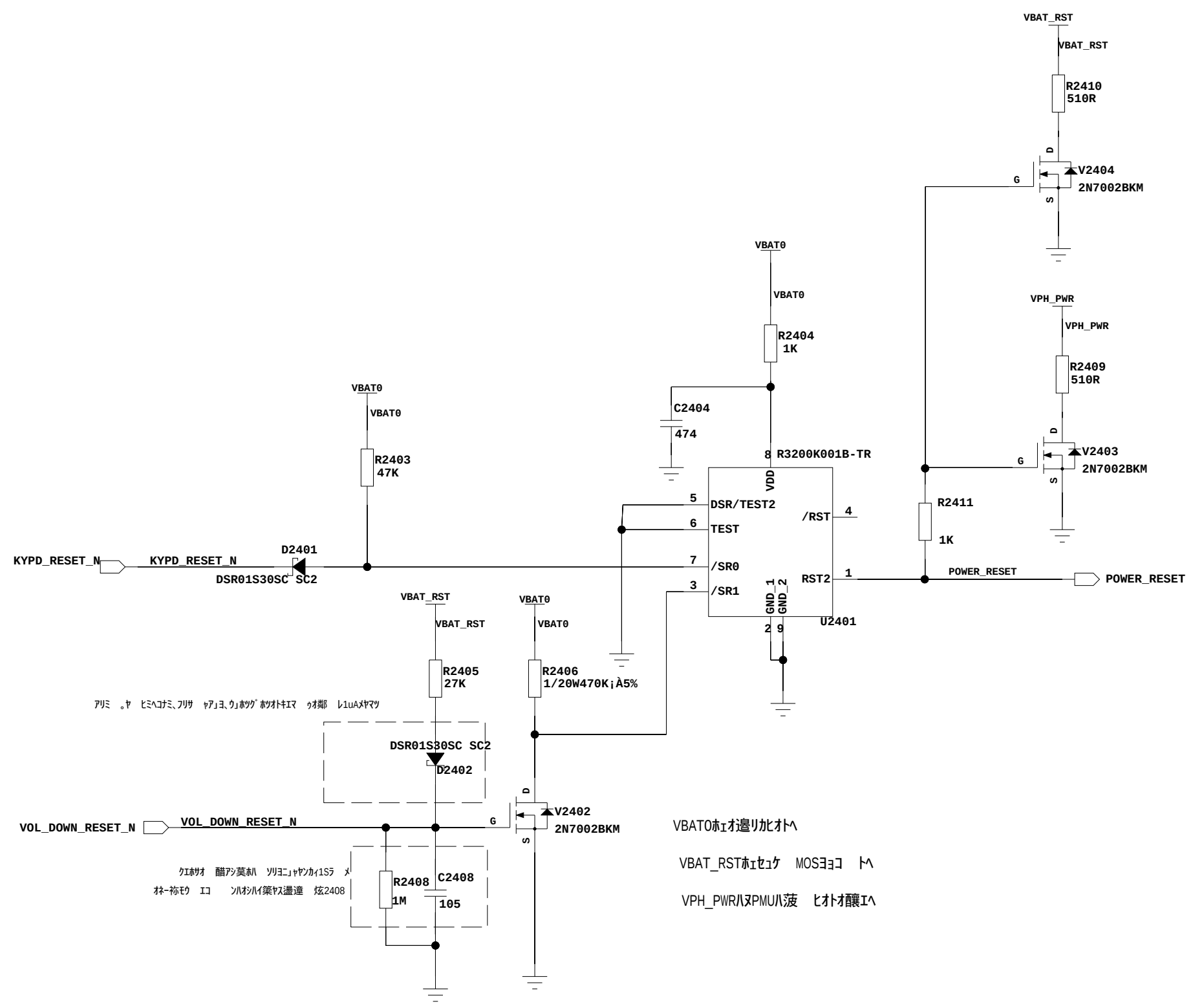
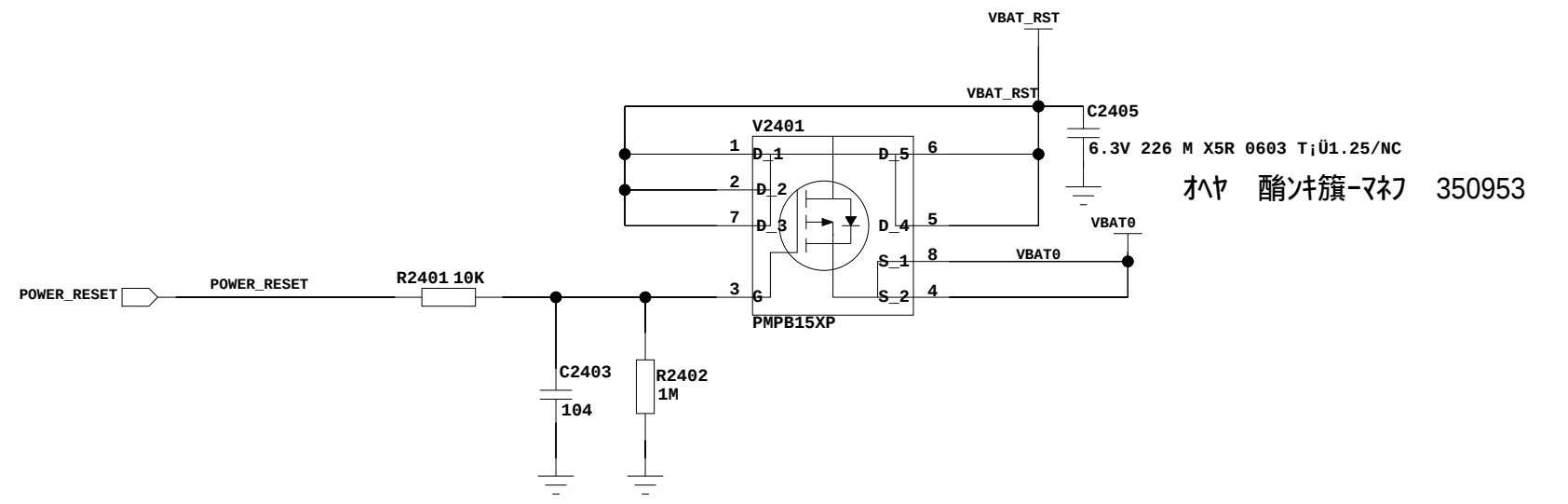
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RESET

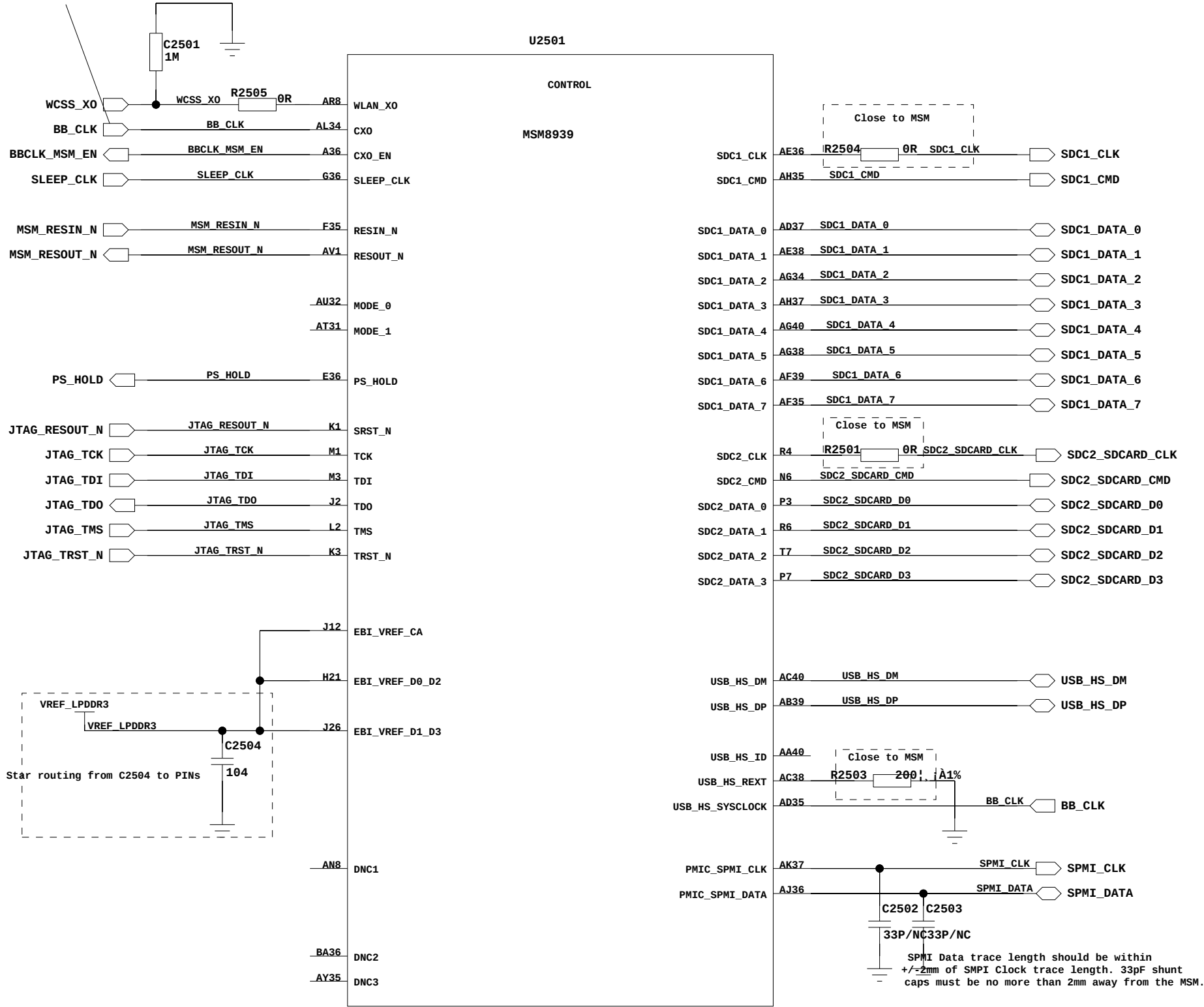


	Sign	Date	Type:
Drawn By			Draw No: 15811
Checked By			Sheet of 30 Ver:
Standard By			oppo
Approved By			

SYS_CTRL_SDC

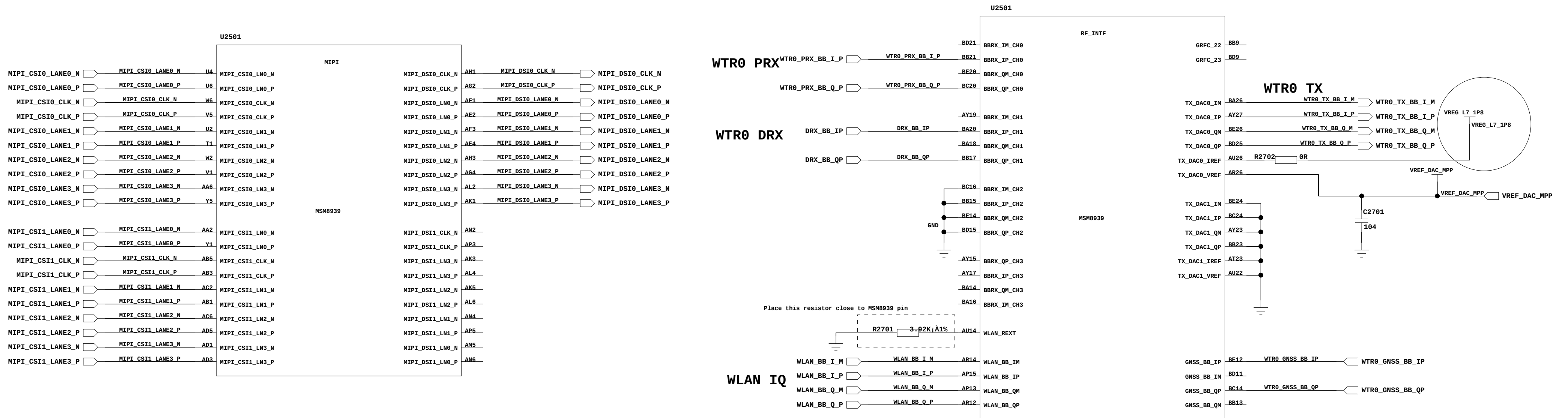
EBI

For BBCLK1, AD35 will be the first load and AL34 is the final load
Should be a Daisy chain route



	Sign	Date	Type:
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Checked By			Sheet of 30 Ver:
Standard By			oppo
Approved By			

MIPI_RF_INTERFACE



	Sign	Date	Type:		
Drawn By			Draw No: 15011		
Checked By			Sheet	of 30	Ver:
Standard By					
Approved By					

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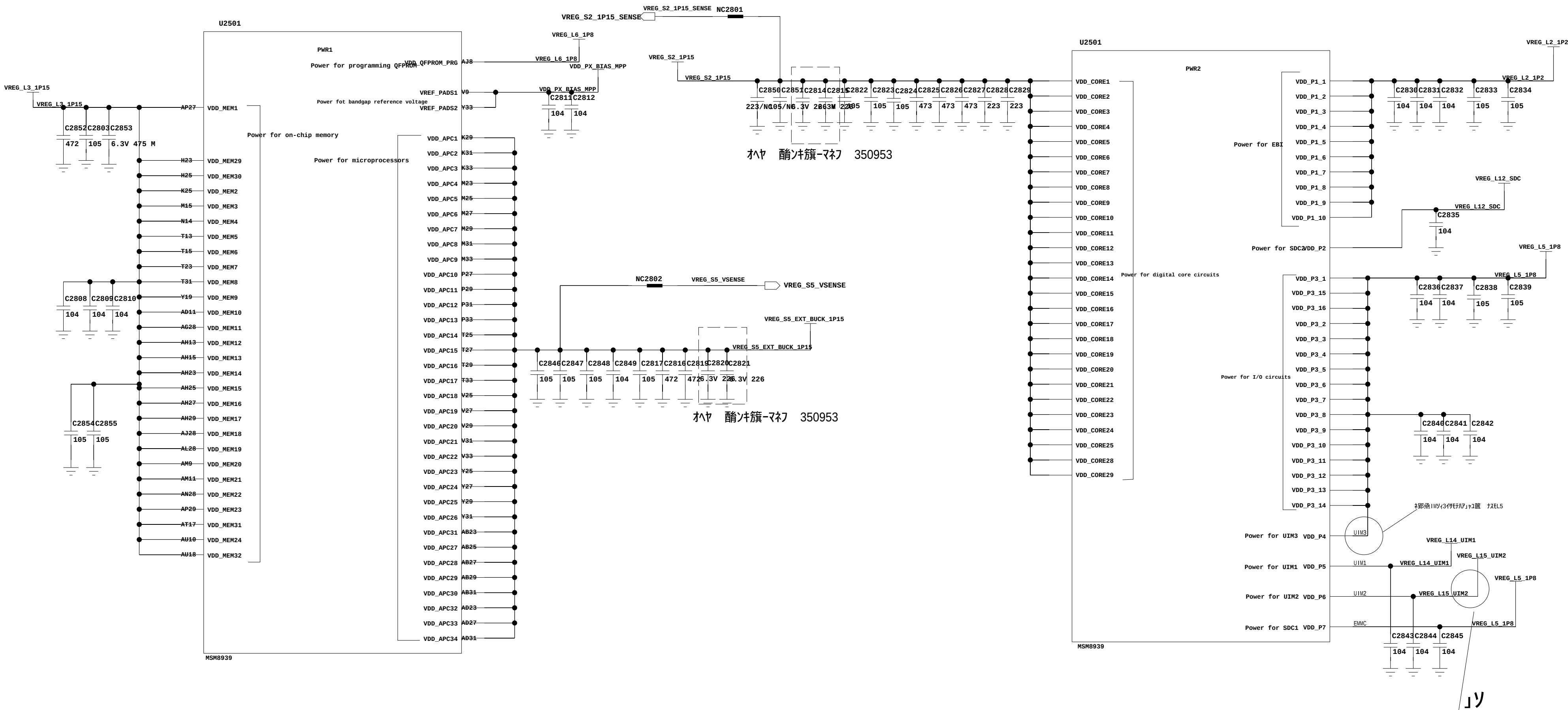
B

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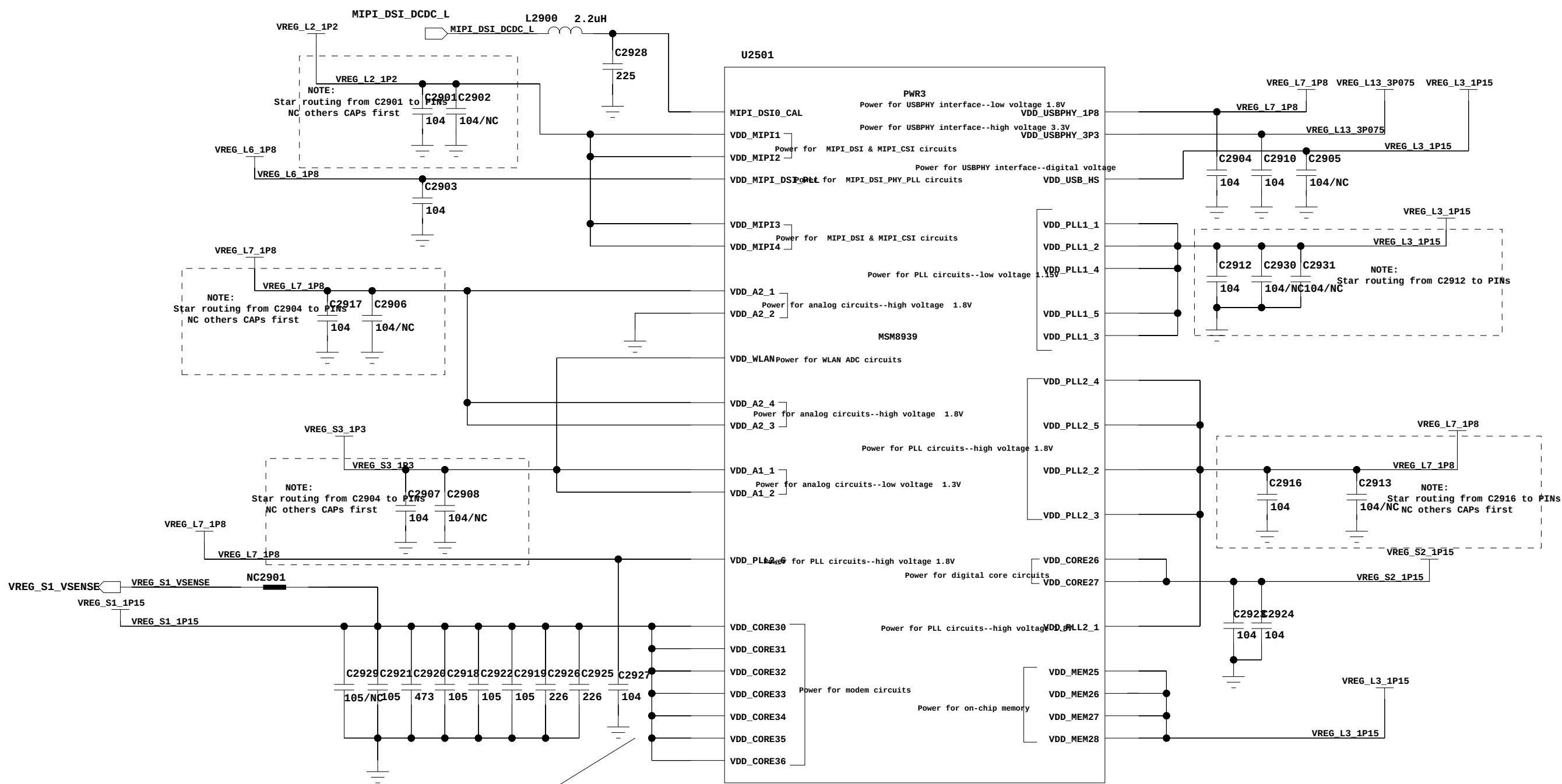
E

F



Regulator	MSM8916/36/39(UIM Only) TSTS without USB UICC Support	MSM8936/39(USB UICC Only) SDA with USB UICC Support
L14	NO	YES
L15	YES	NO

	Sign	Date	Type:
Drawn By			Draw No: 15011
Checked By			Sheet of 30 Ver:
Standard By			oppo
Approved By			



Power for modem circuit

PAD#	MSM8916 name	MSM8939 name
AB11, AB19	NC*	VDD_PLL1
P23, P25, G22	NC*	VDD_PLL2
AK25, AK27, AM27, AP25 AT25, M19, P19	NC*	VDD_MODEM

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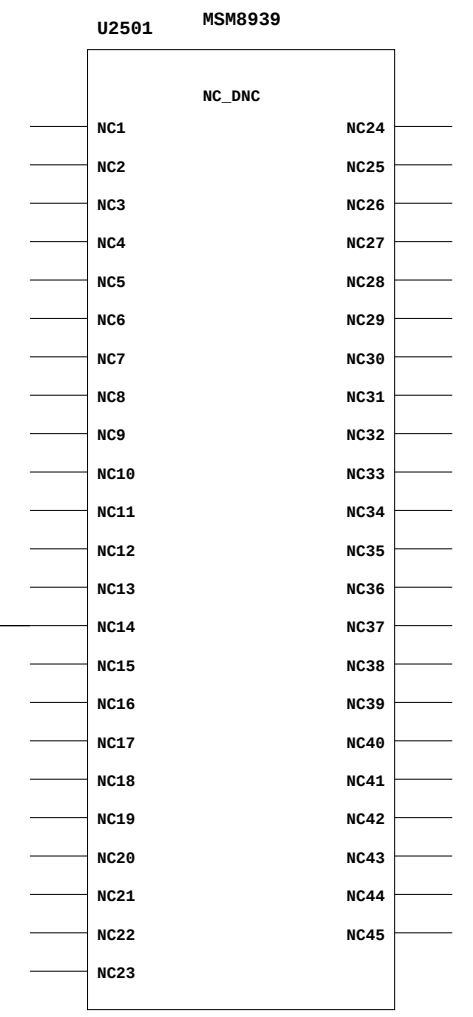
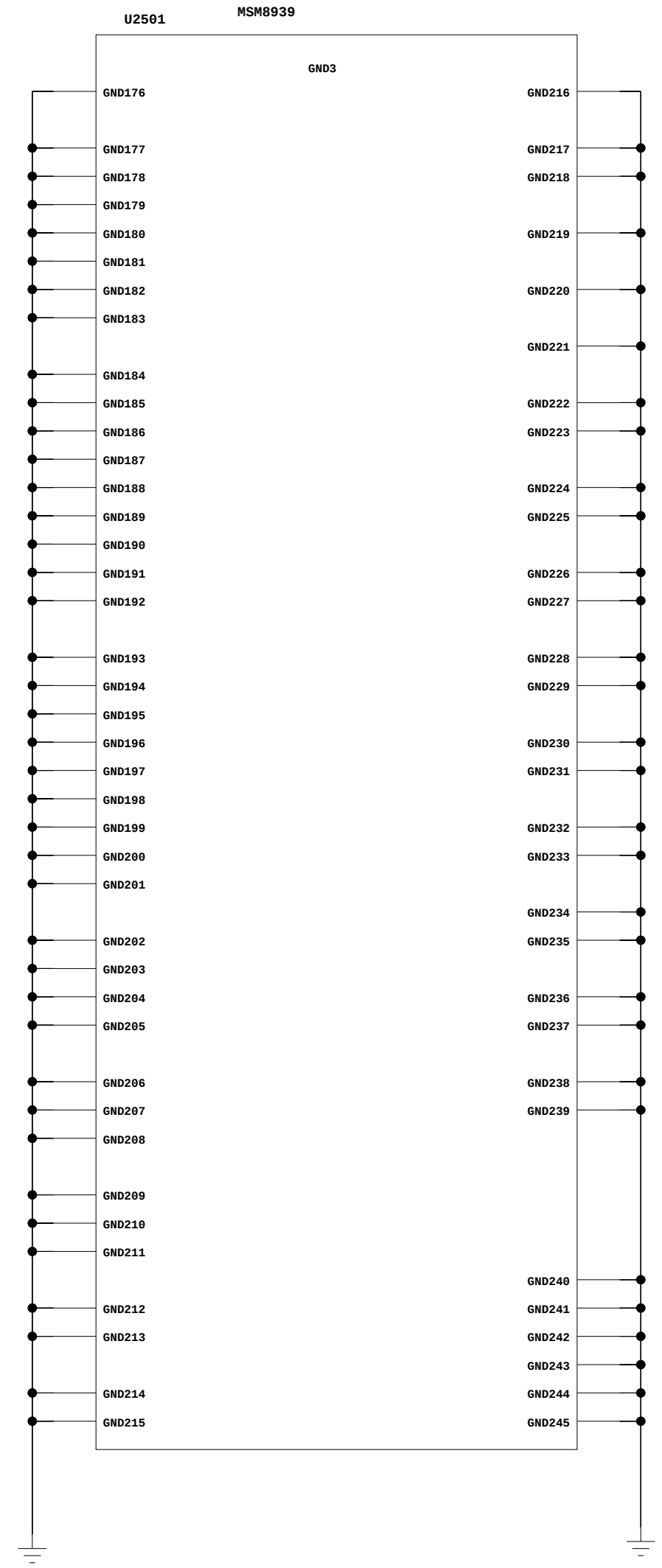
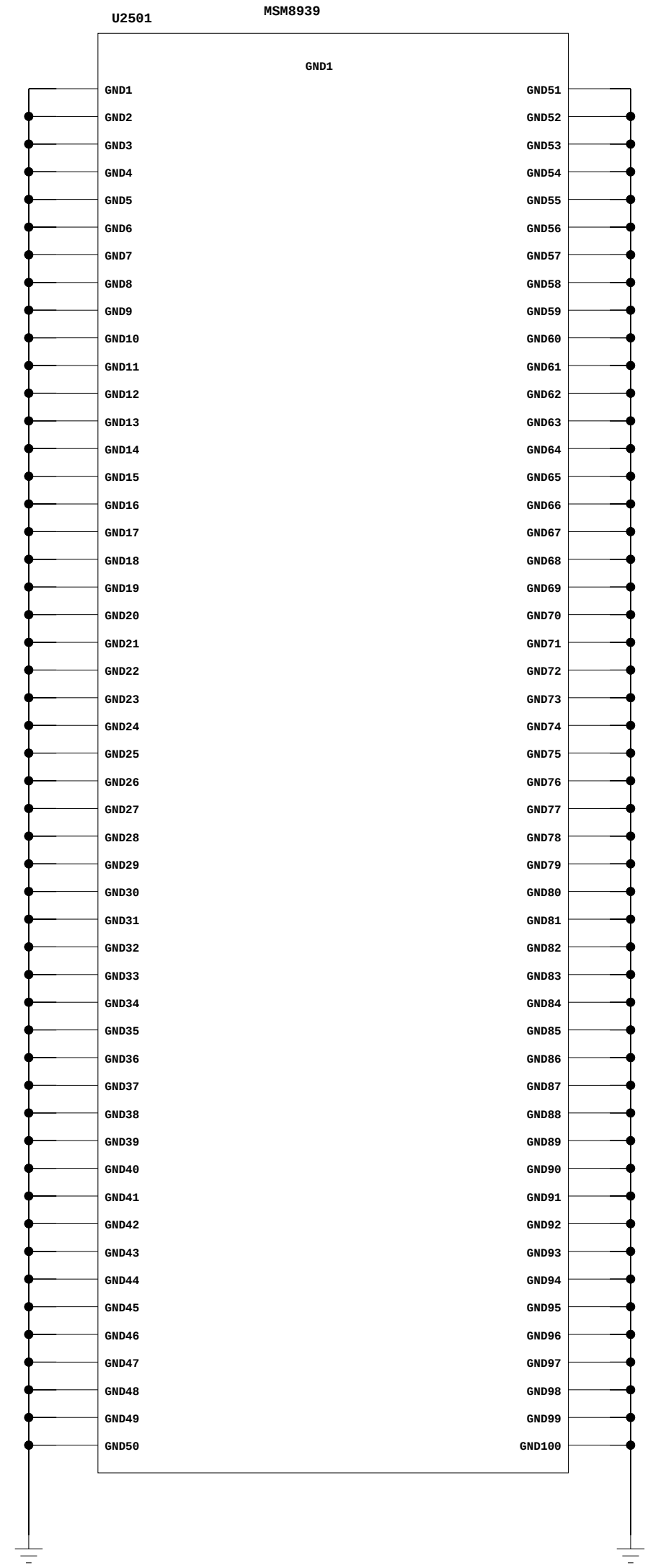
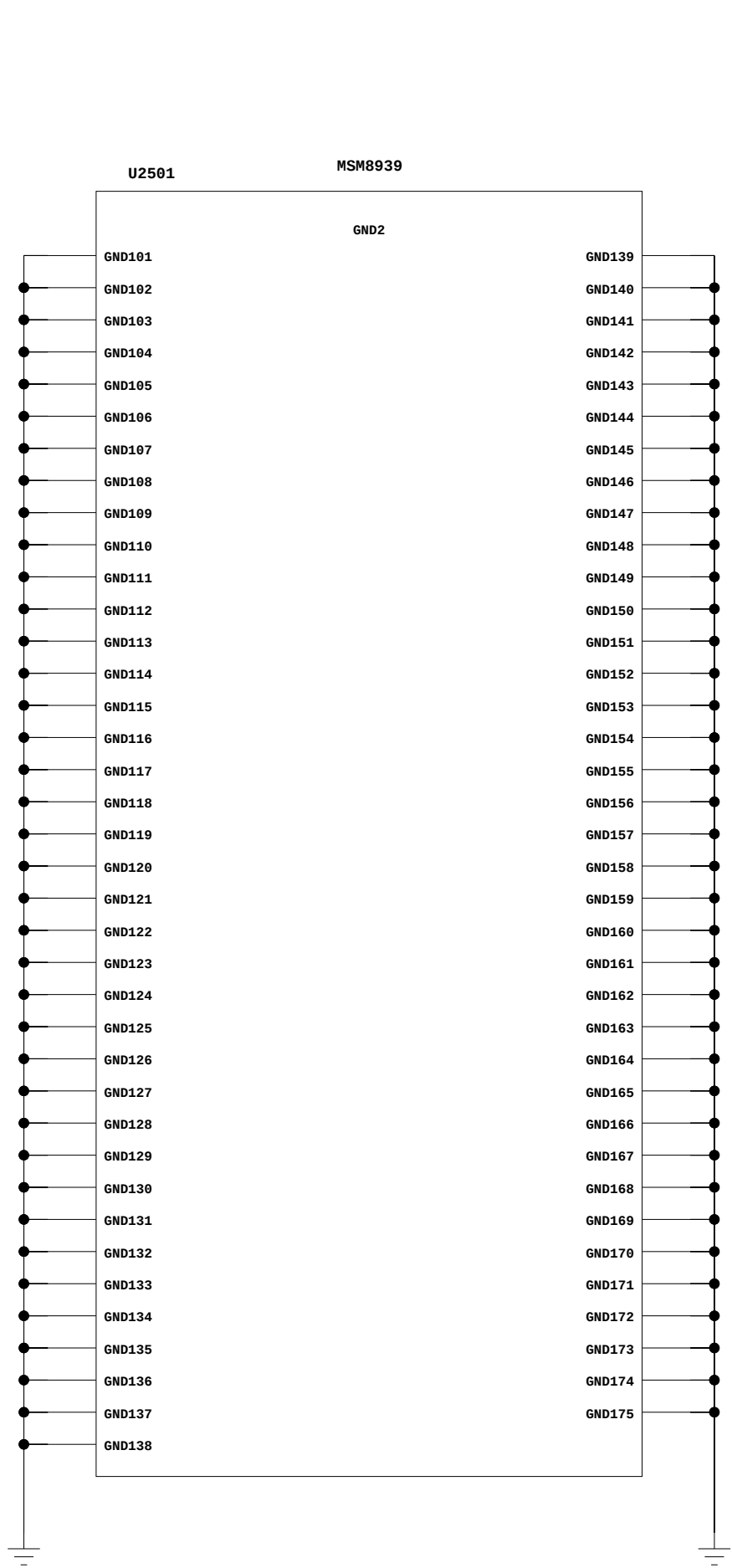
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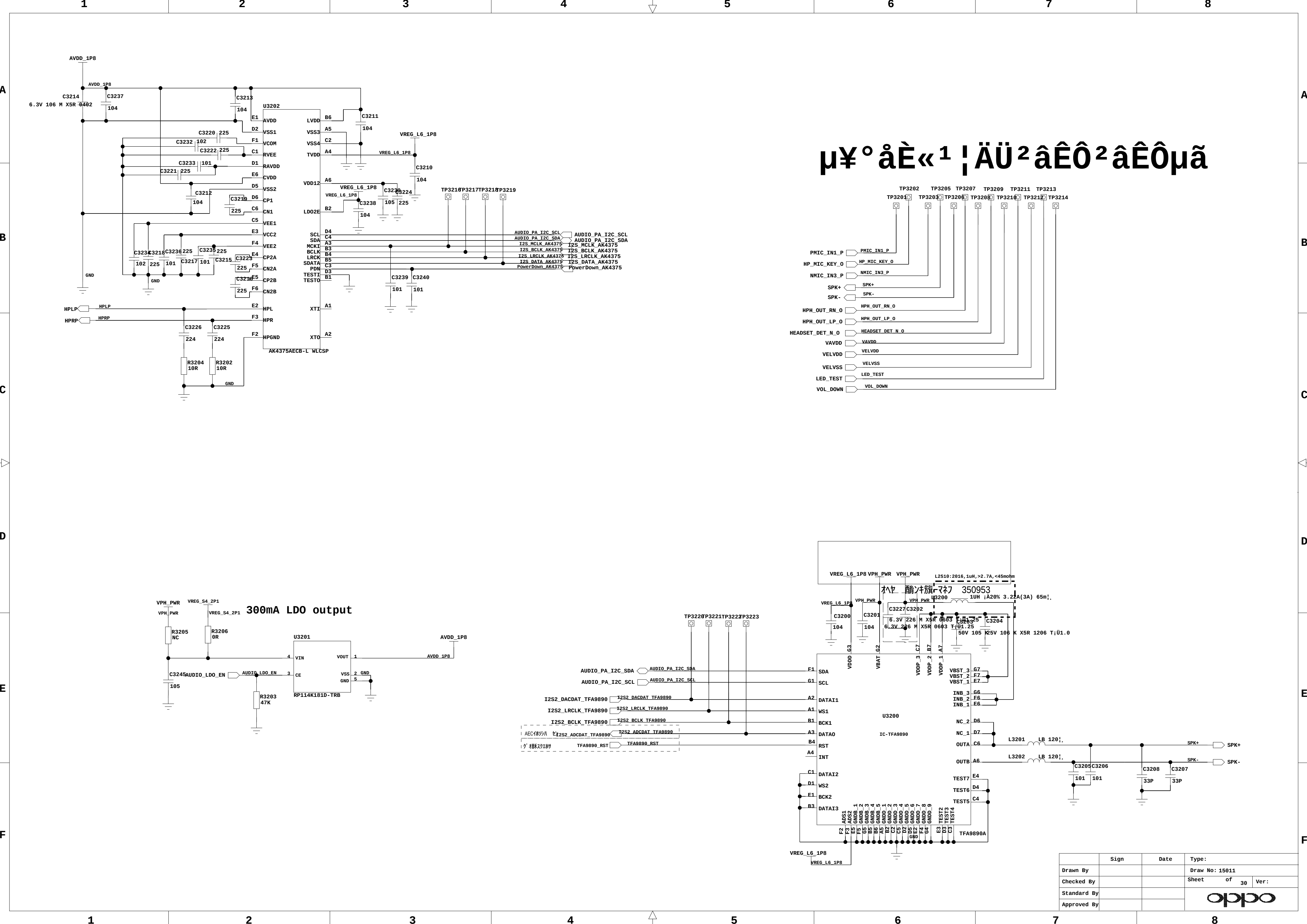
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MIPI_DSI_DCDC<MIPI_DSI_DCDC L

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300mA LDO output

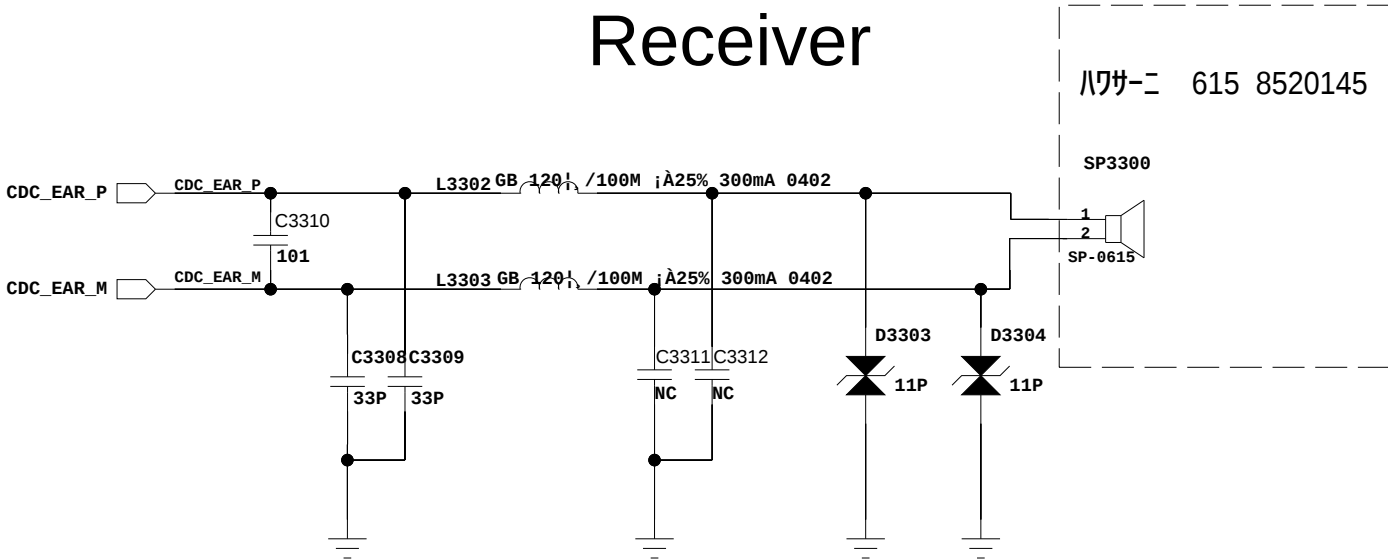
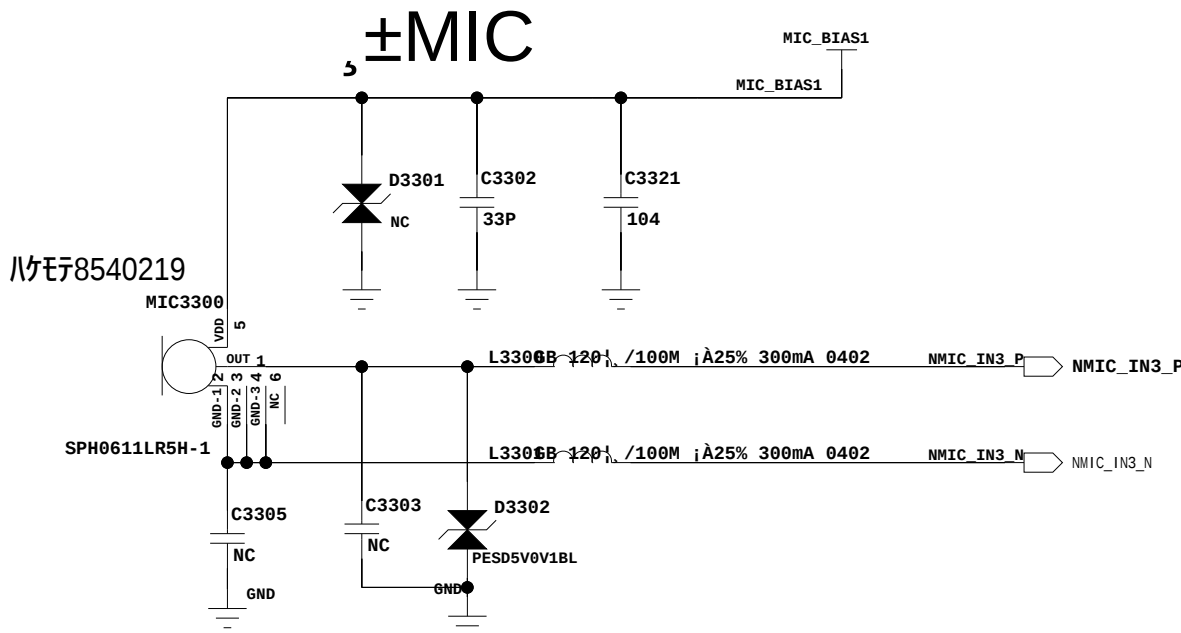
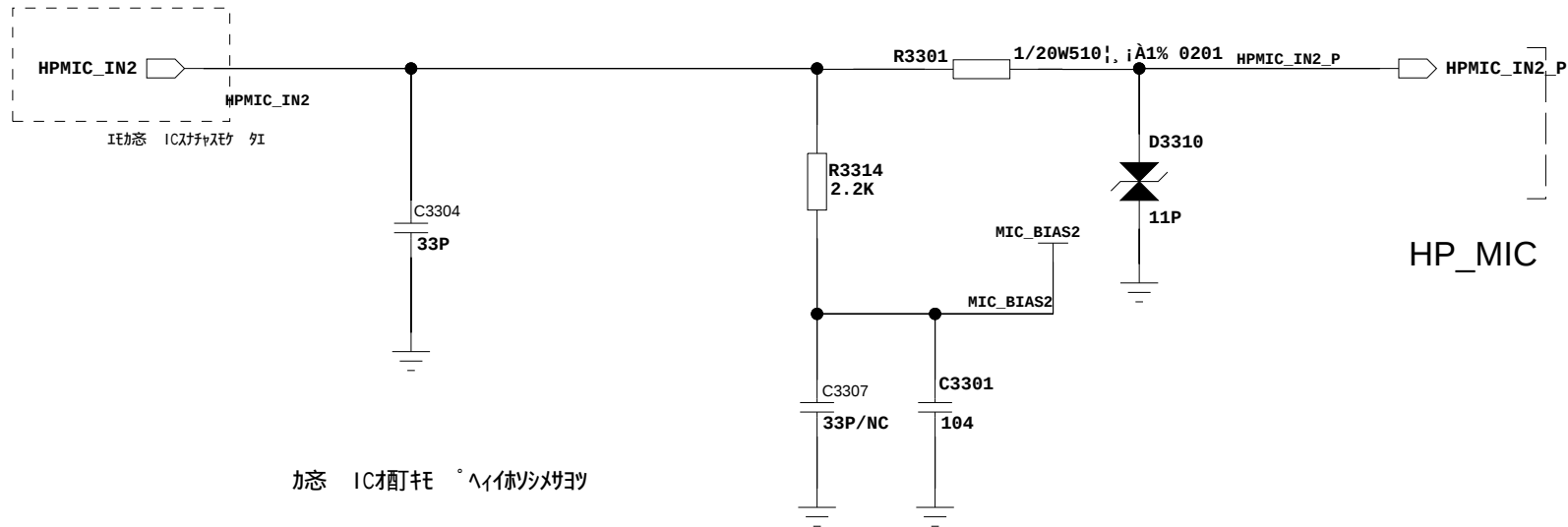
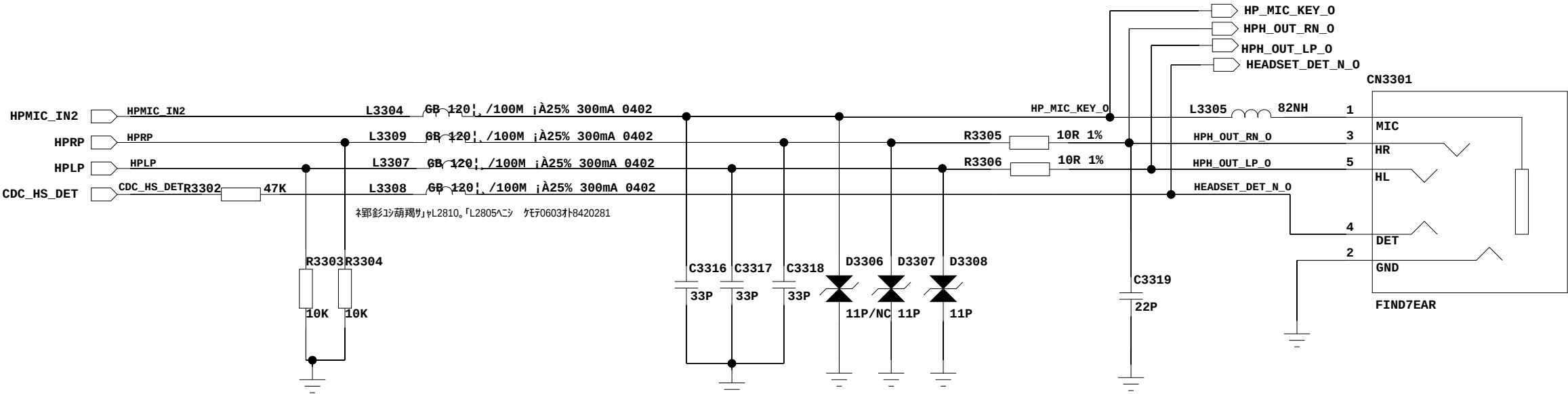
350953

AUDIO_PA_I2C_SDA
AUDIO_PA_I2C_SCL
I2S2_DACDAT_TFA9890
I2S2_LRCLK_TFA9890
I2S2_BCLK_TFA9890
TFA9890_RST

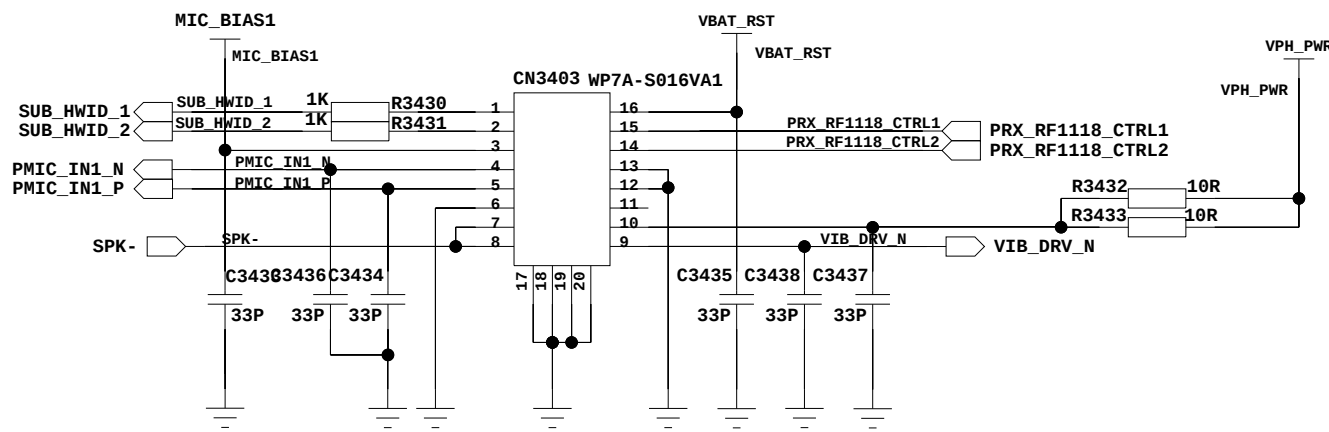
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Standard By			oppo
Approved By			

HeadPhone

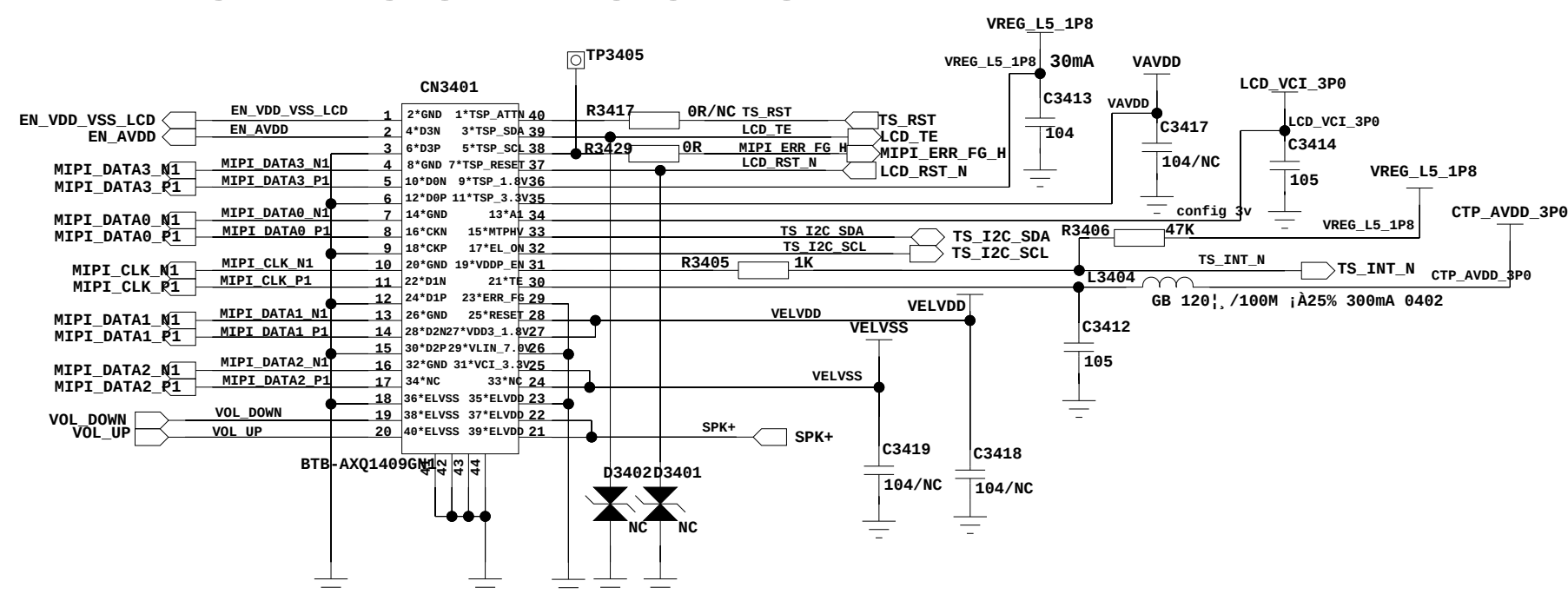
HP MIC



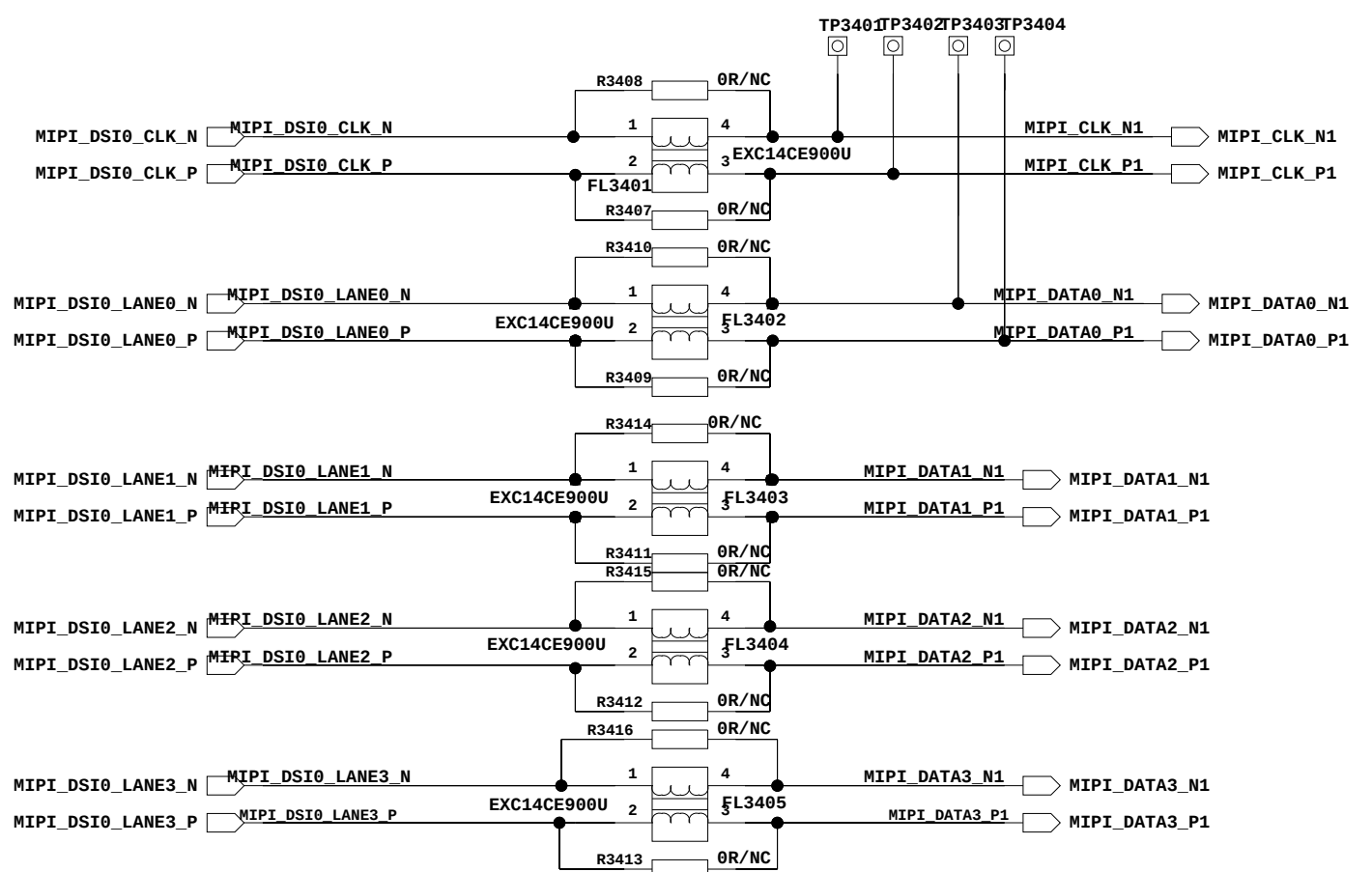
Sub board connector



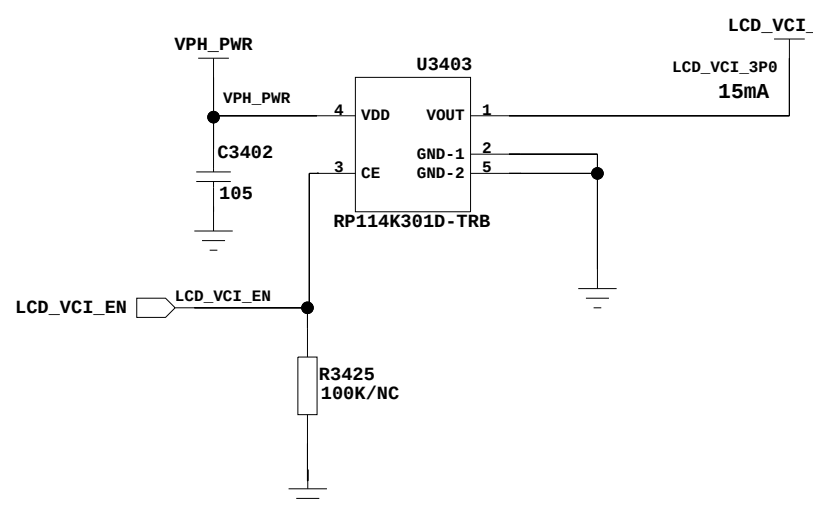
LCM Connector



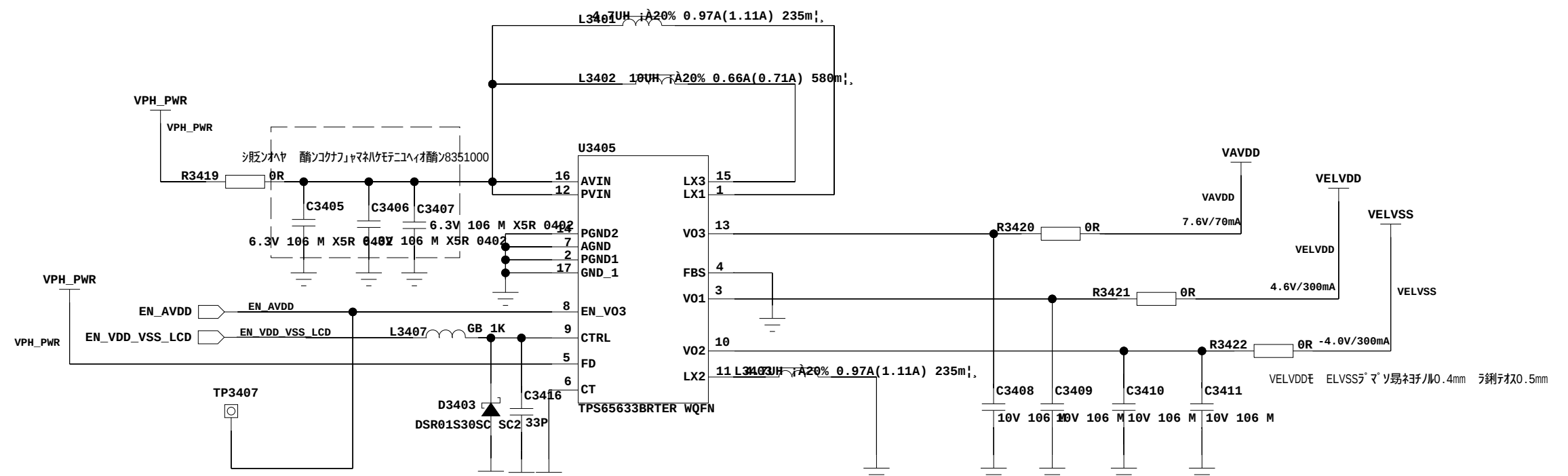
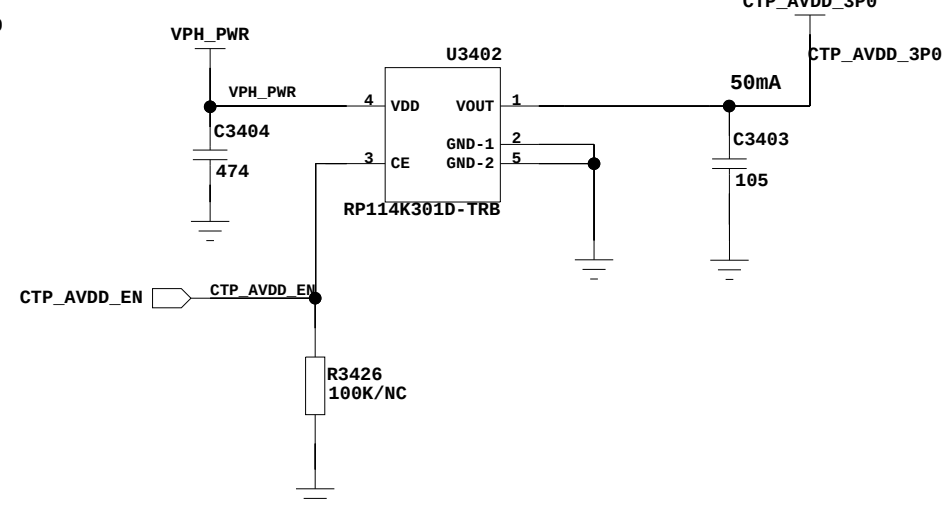
MIPIイ築材羸。チソソス鮪TB化



LCD AVDD (3.0V)



CTP AVDD 3.0V



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Standard By					
Approved By					

A



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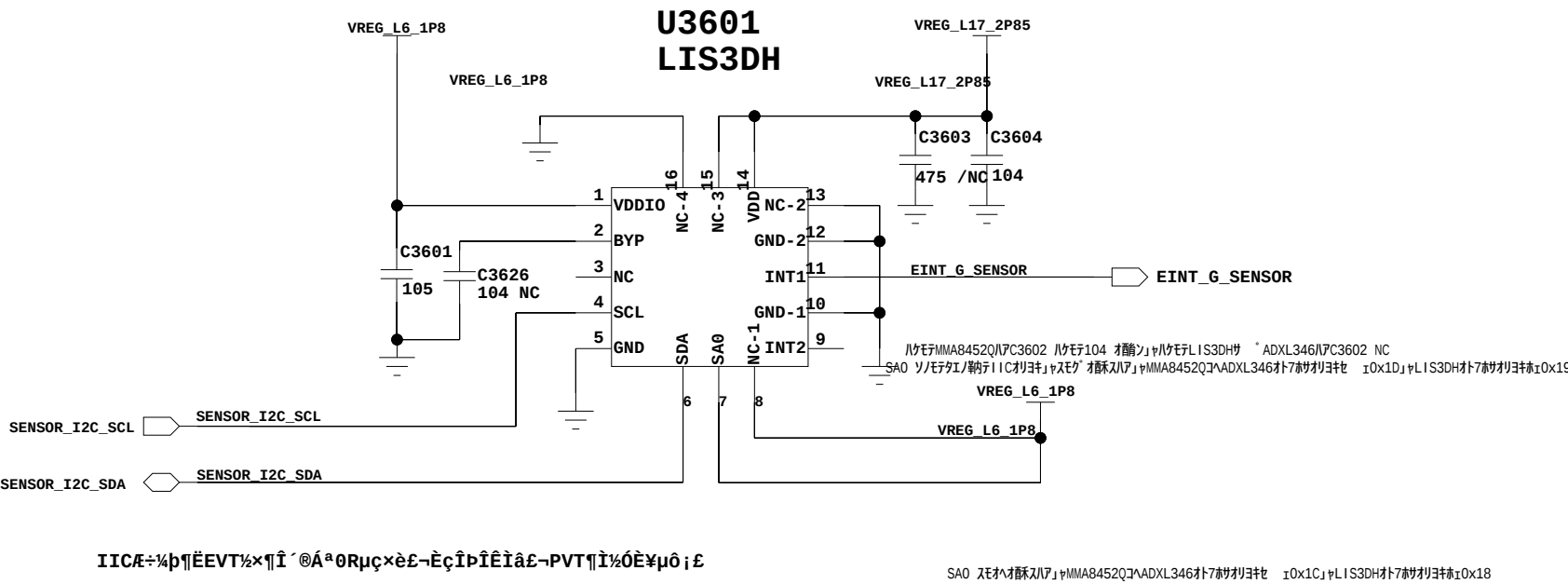


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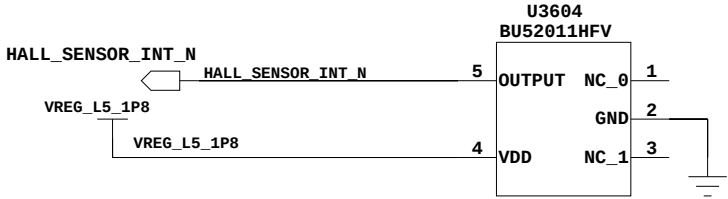
8

G- sensor

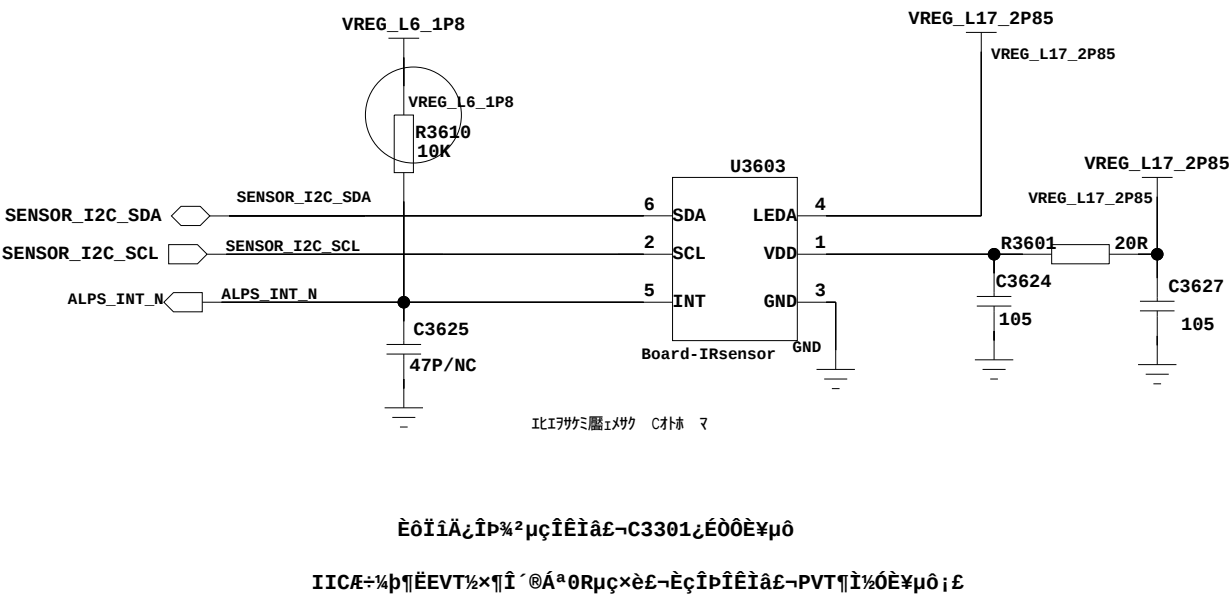
LIS3DH Slave address 0X19(0011001)



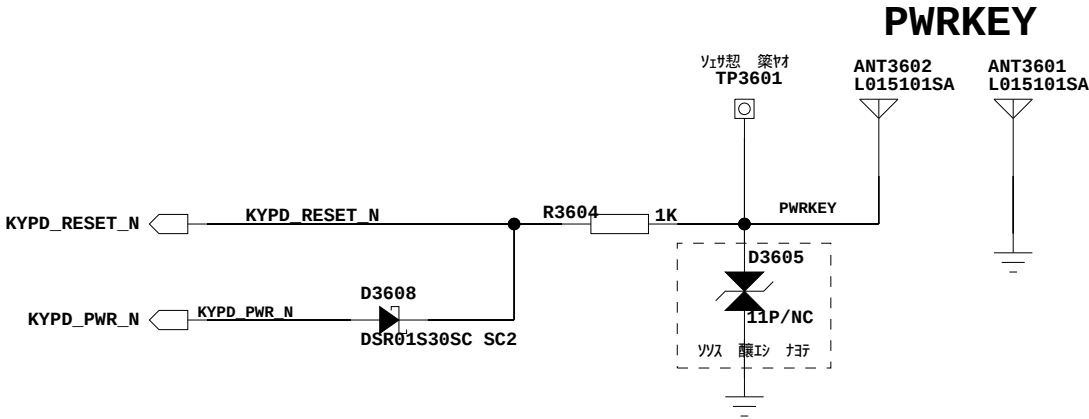
Hall sensor



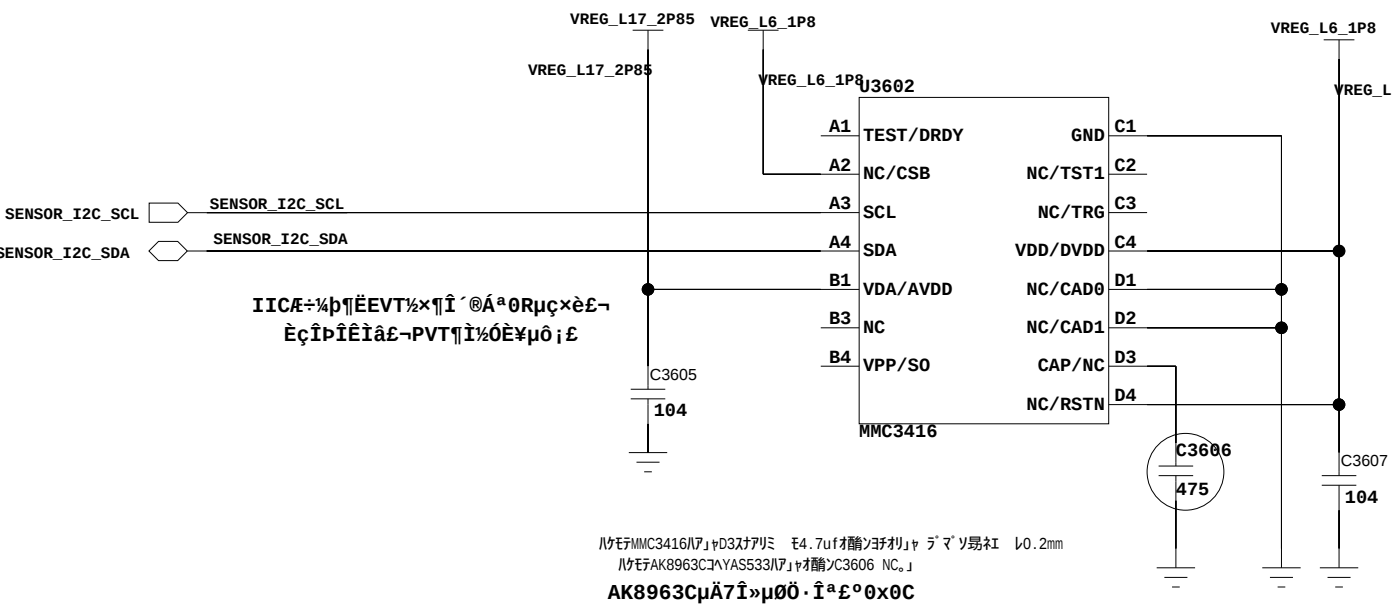
A.L.S.+P.S sensor module BTB



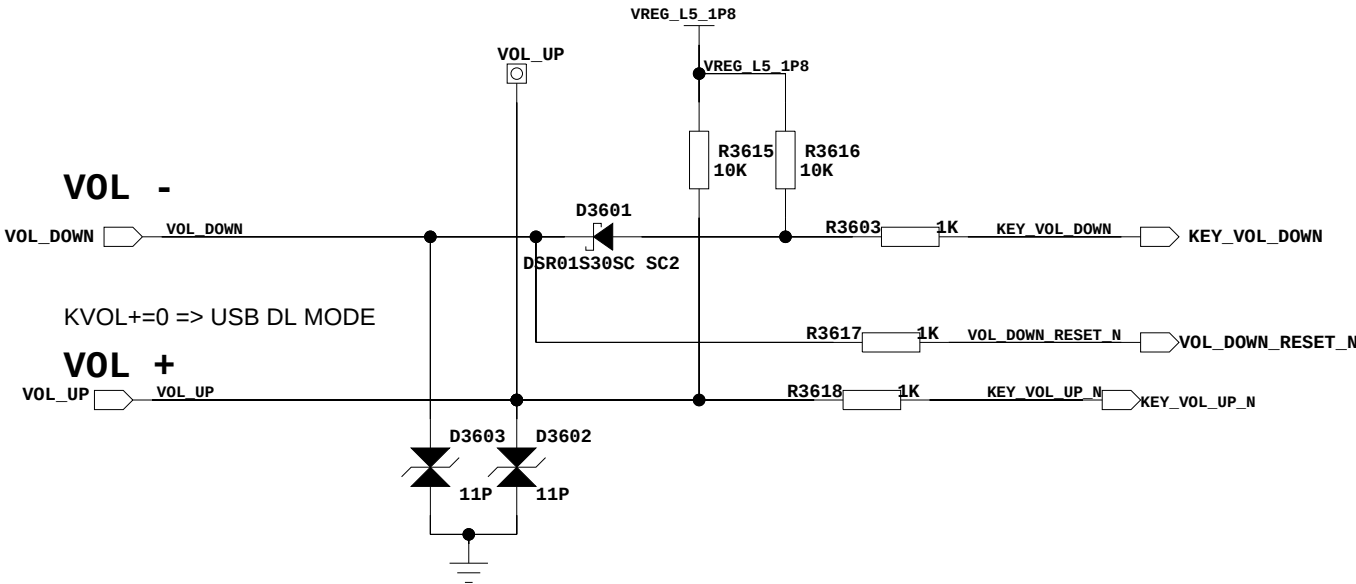
POWER KEY



M- sensor

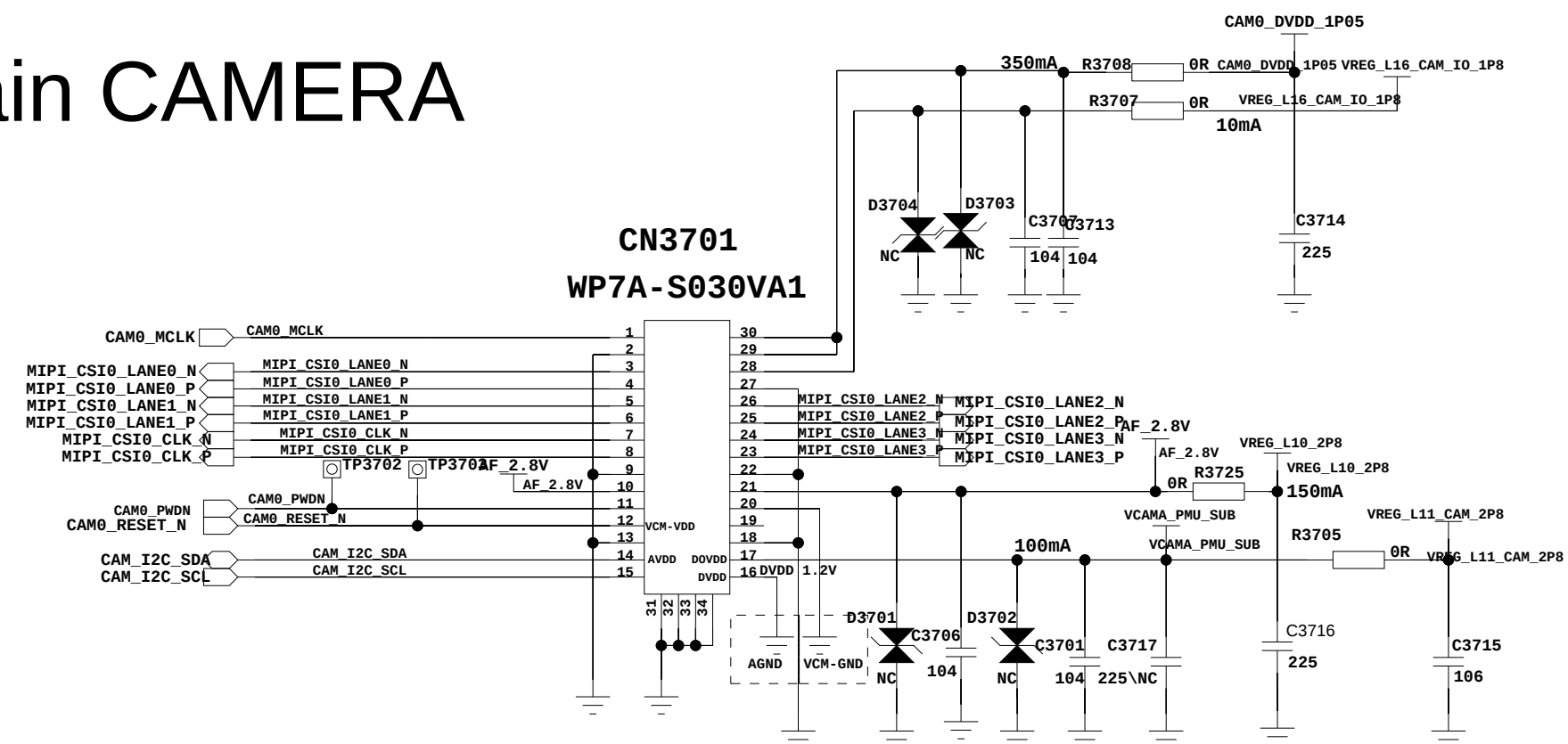


VOL+/- KEY

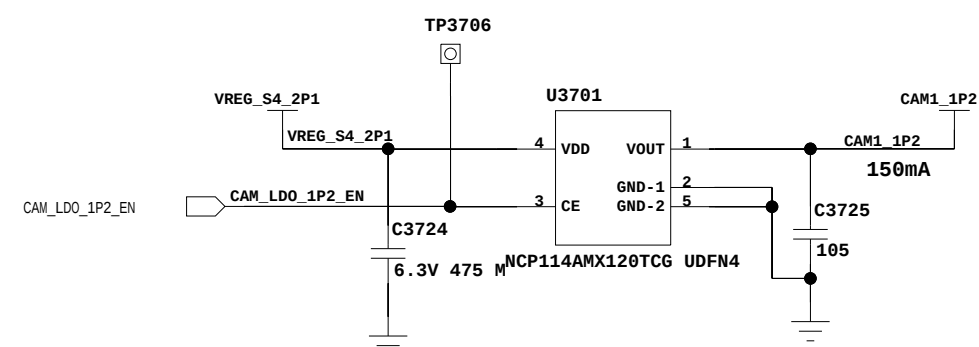
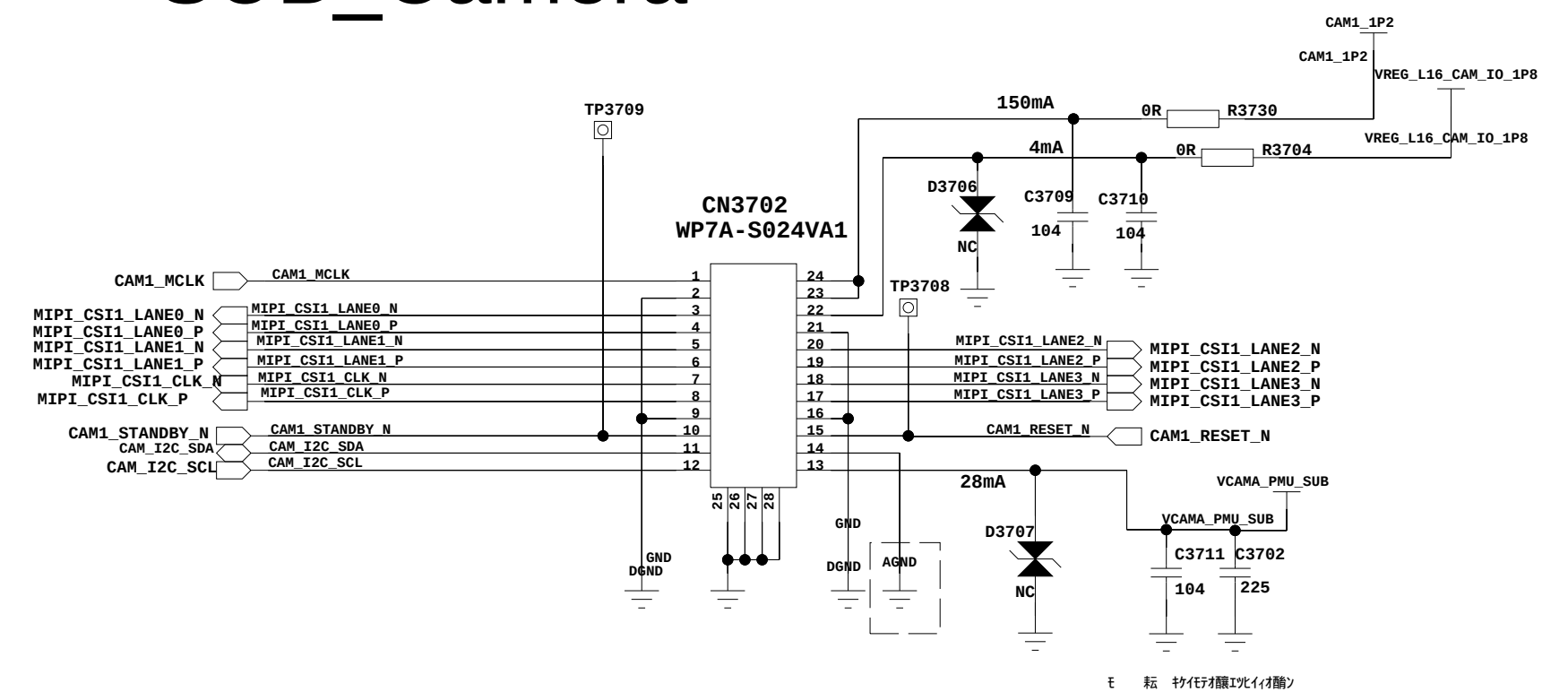


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Checked By			Sheet 26 of 30
Standard By			Ver:
Approved By			oppo

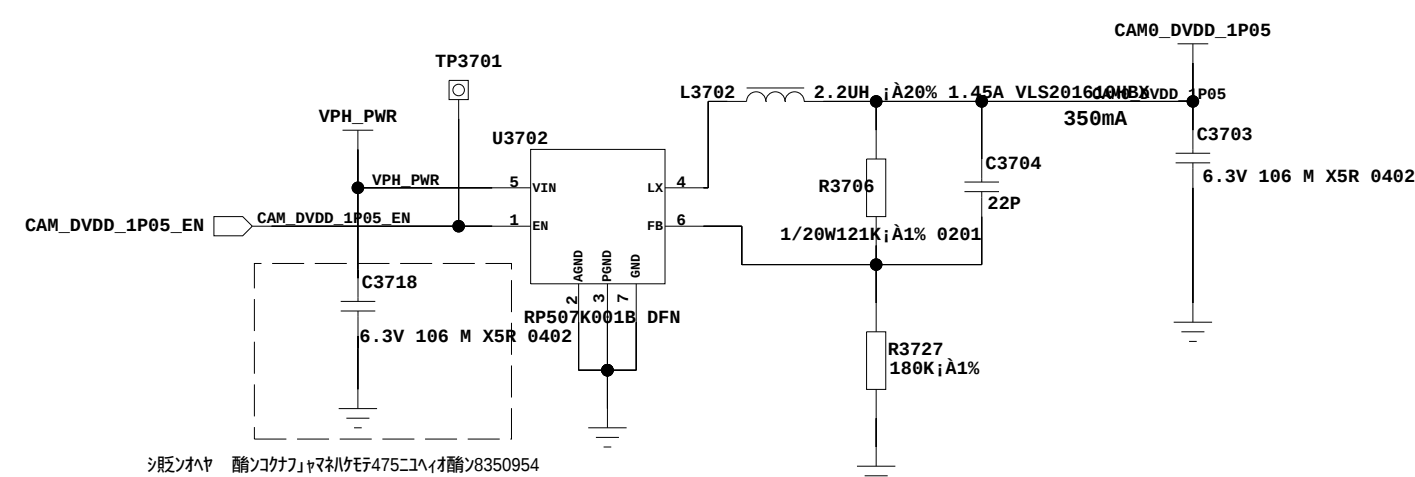
Main CAMERA



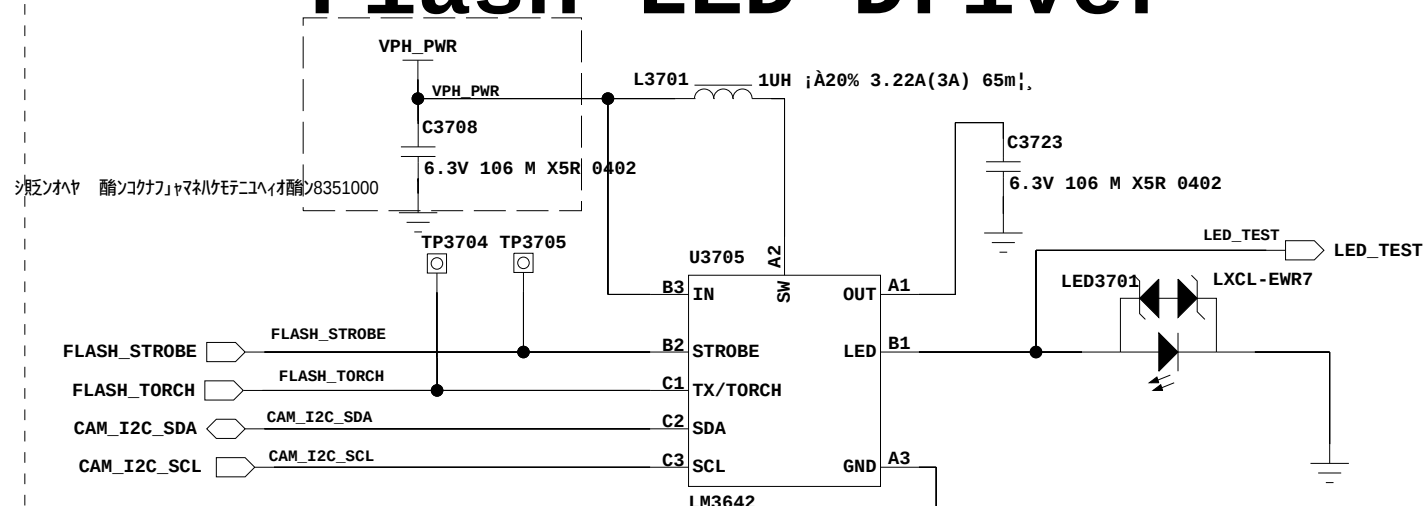
SUB_Camera



Main Camera DVDD(1.05V)



Flash LED Driver



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Drawn By			Draw No: 15011
Checked By			Sheet 27 CAMERAPDF LEB:
Standard By			
Approved By			

A

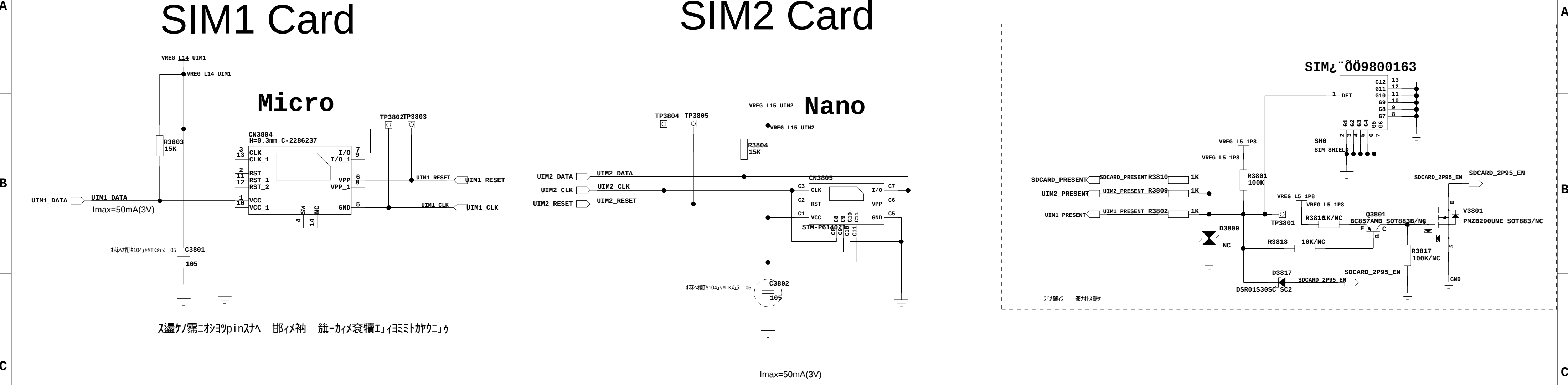
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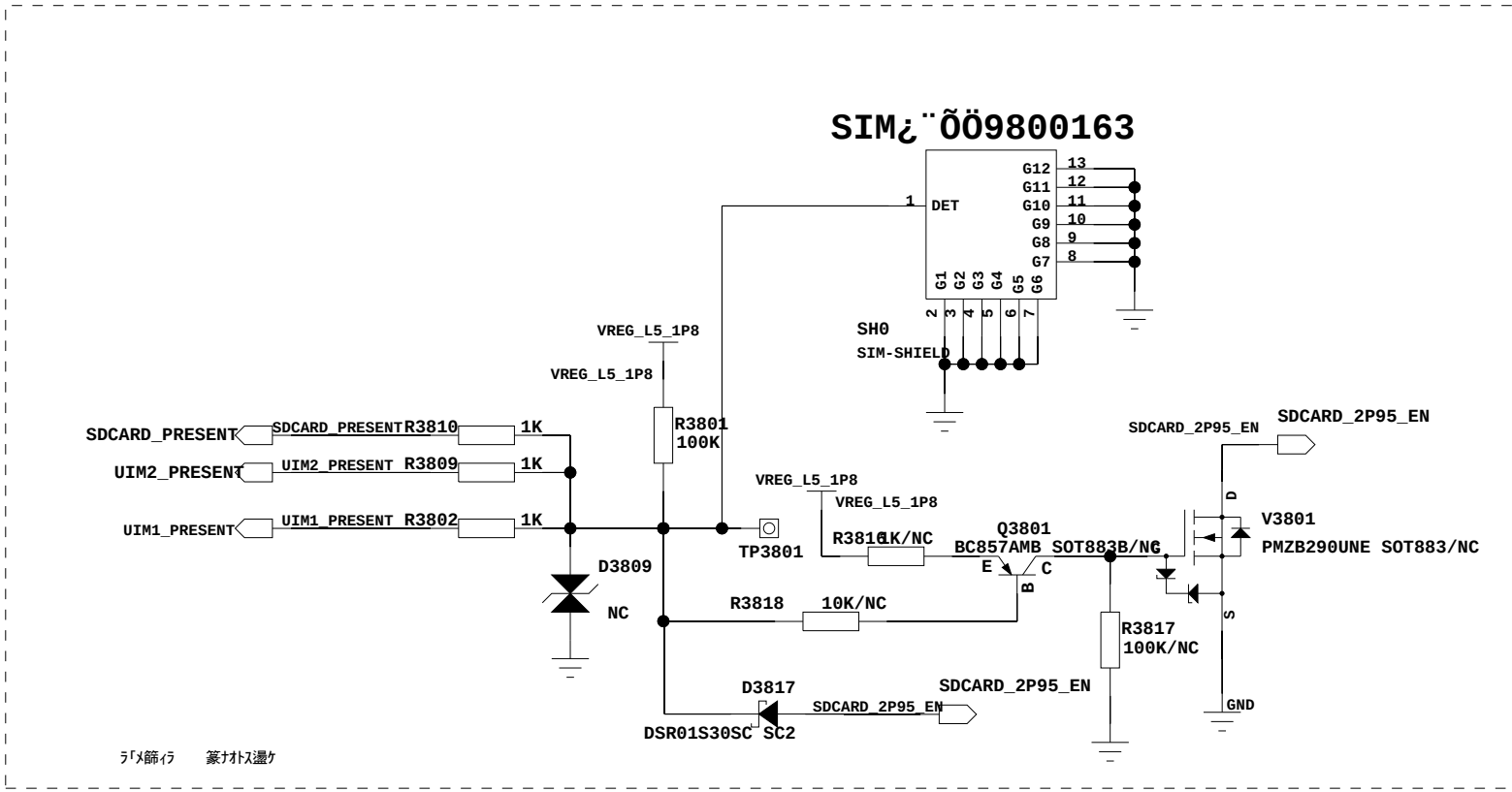
B

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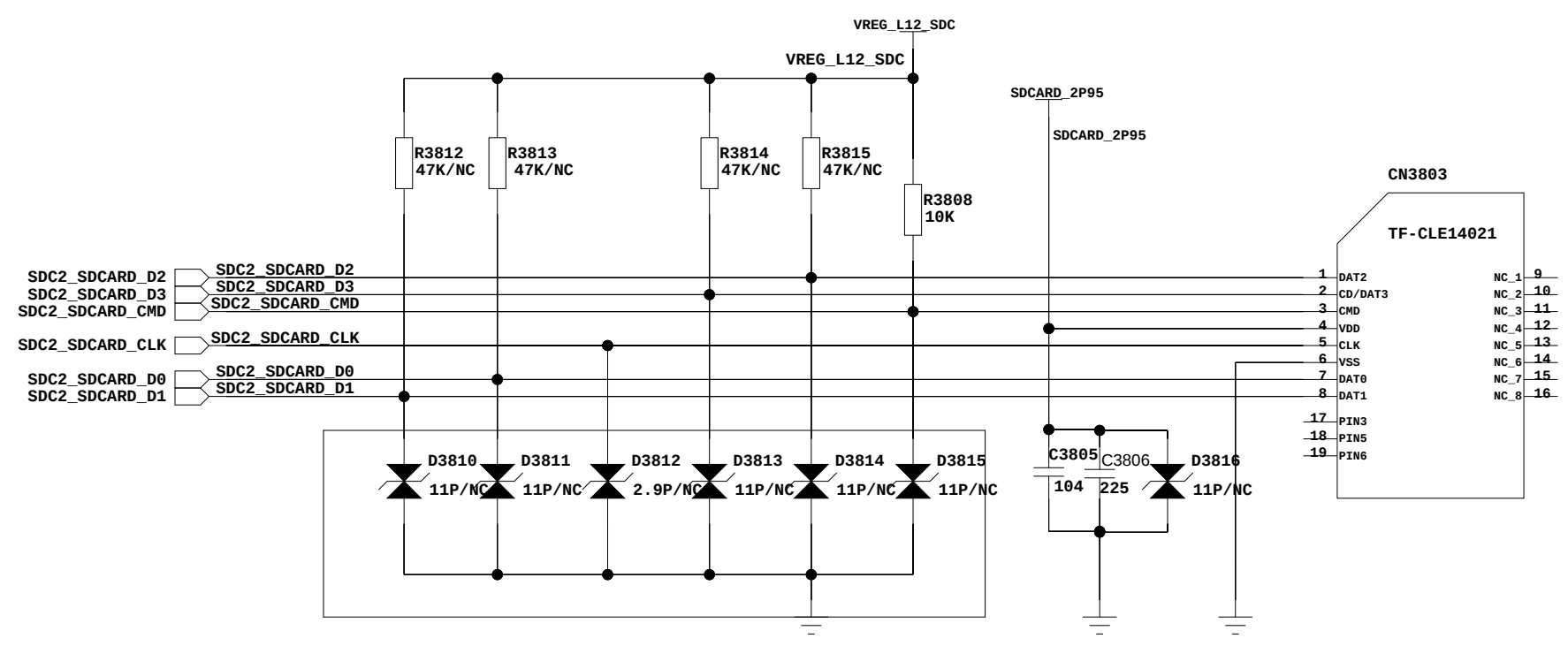
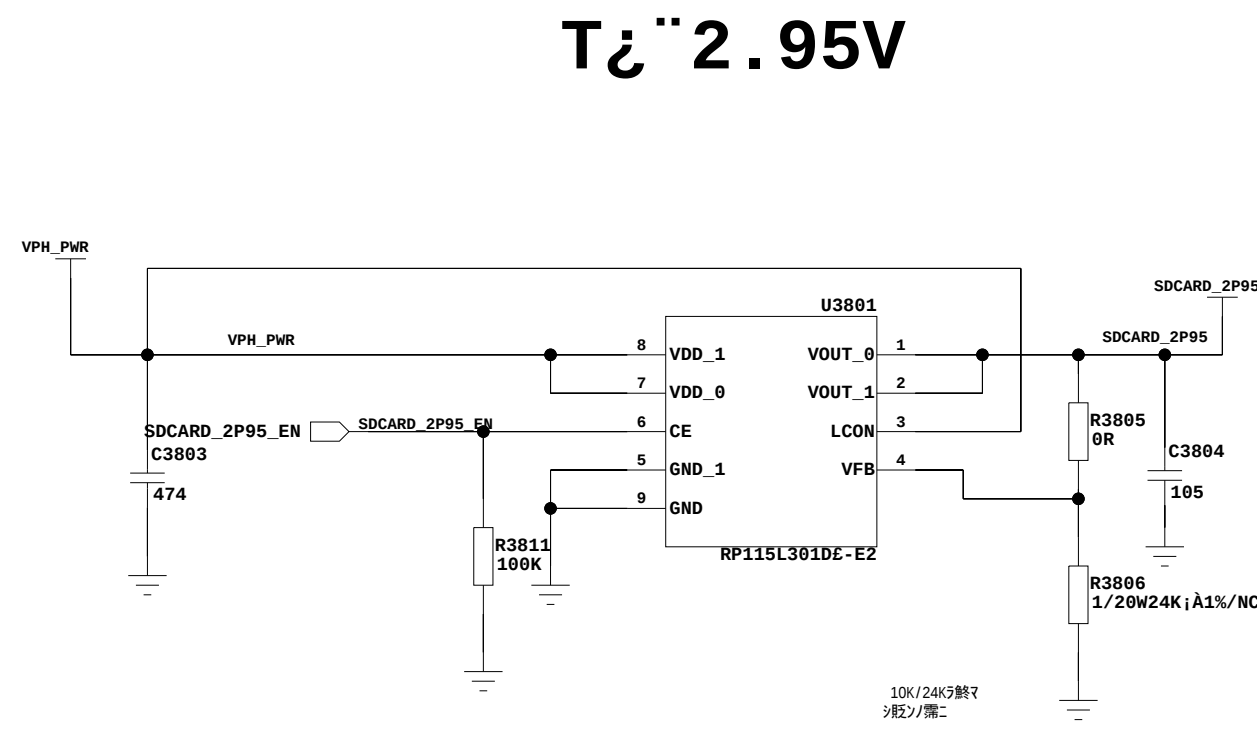
D

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TF_CARD



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Standard By			oppo
Approved By			

FAST CHARGE

A

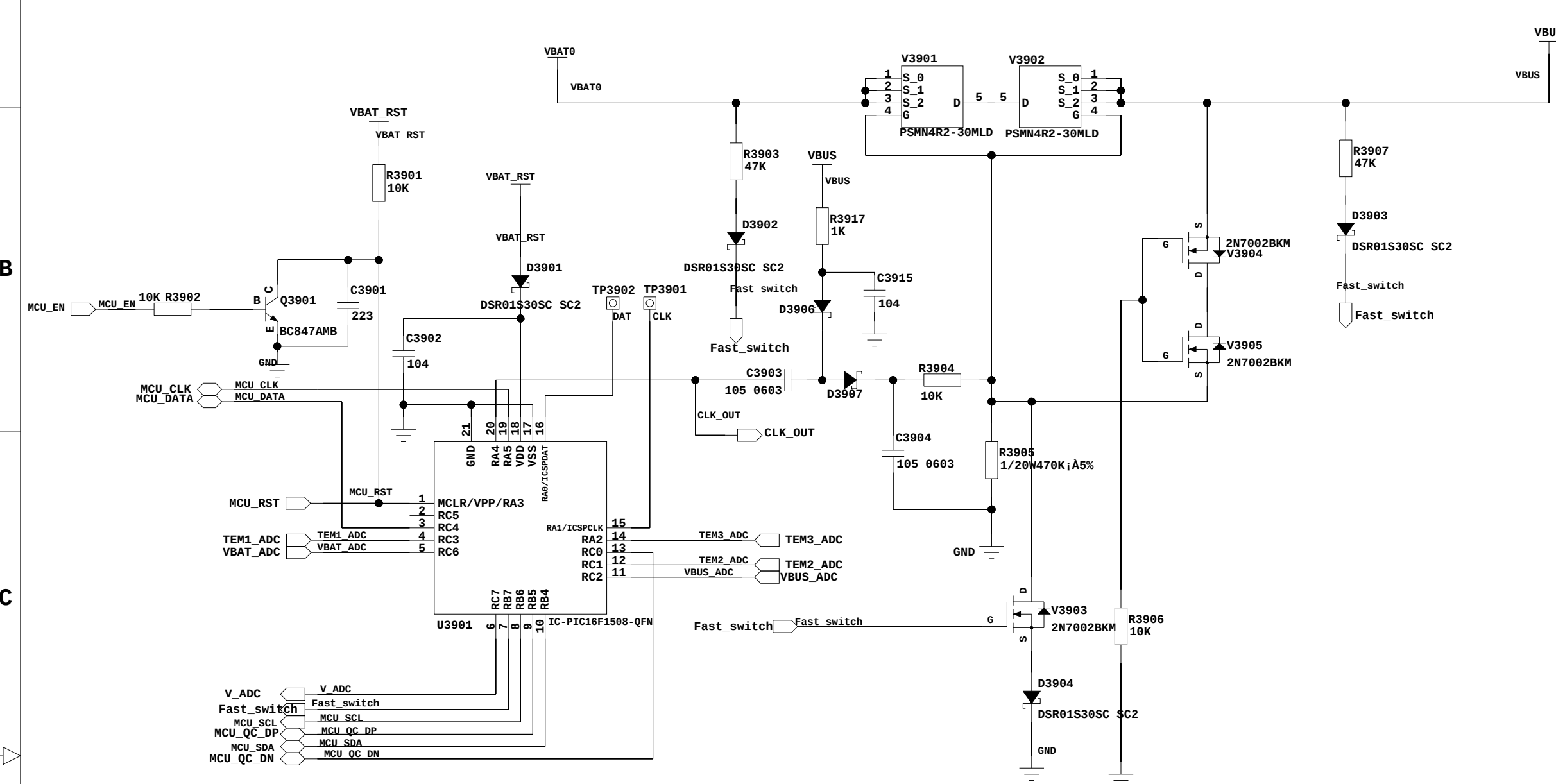
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ADC,×βïß×¢Òâ°üμØ

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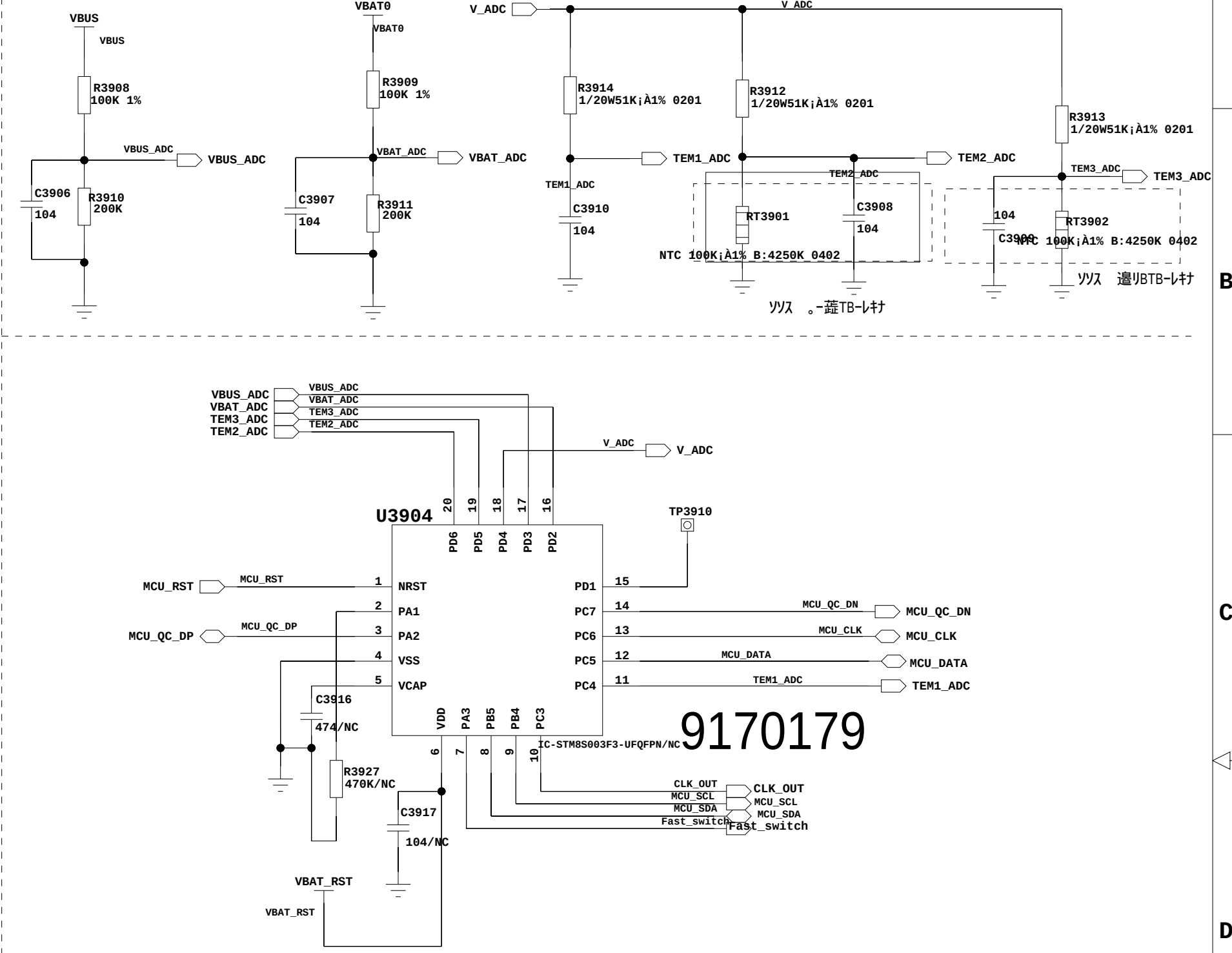
B

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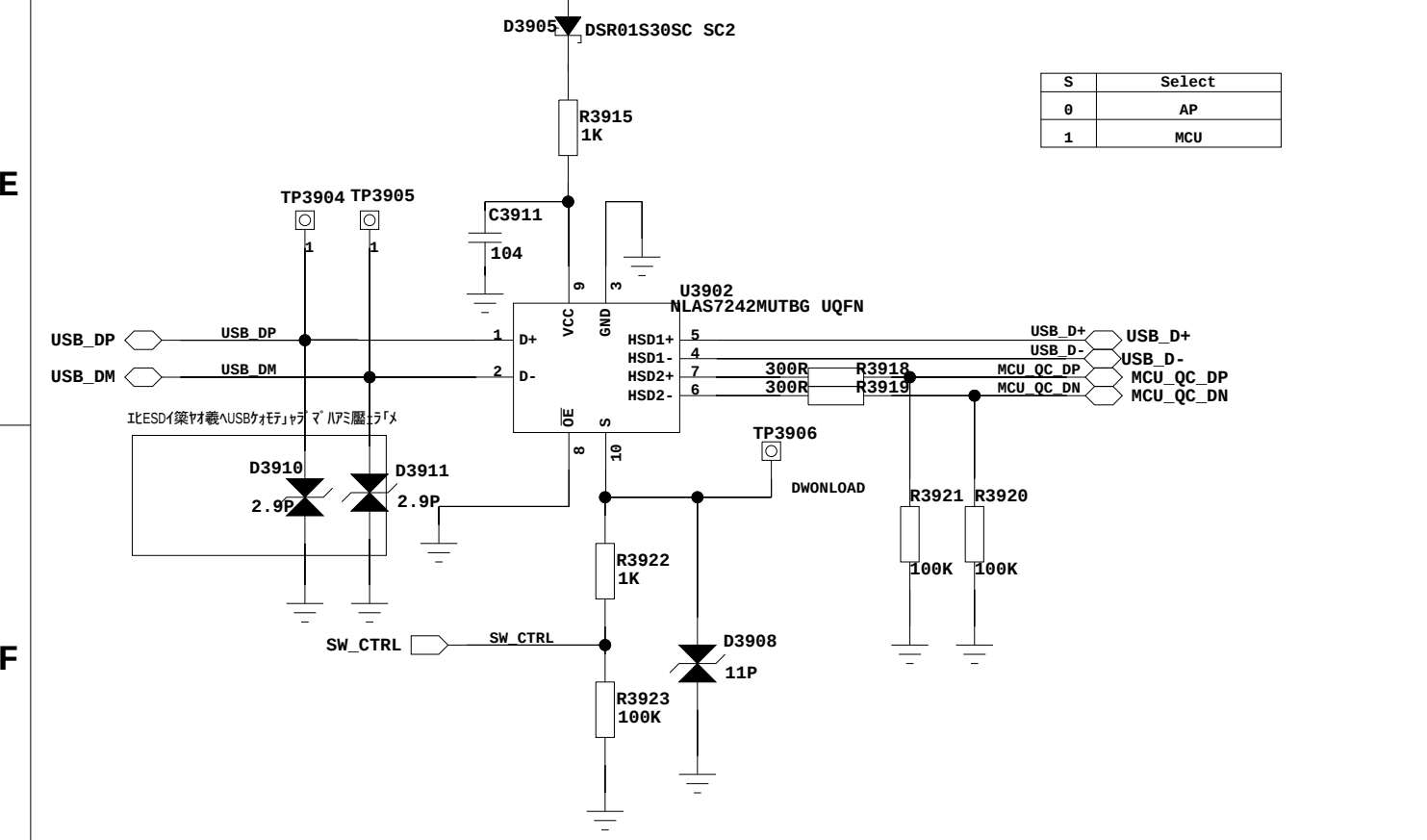
D

E

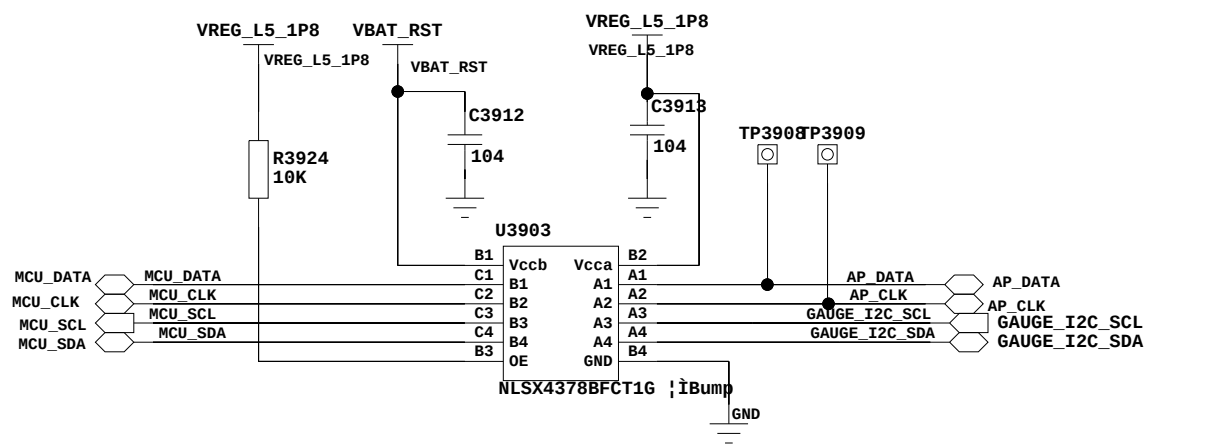
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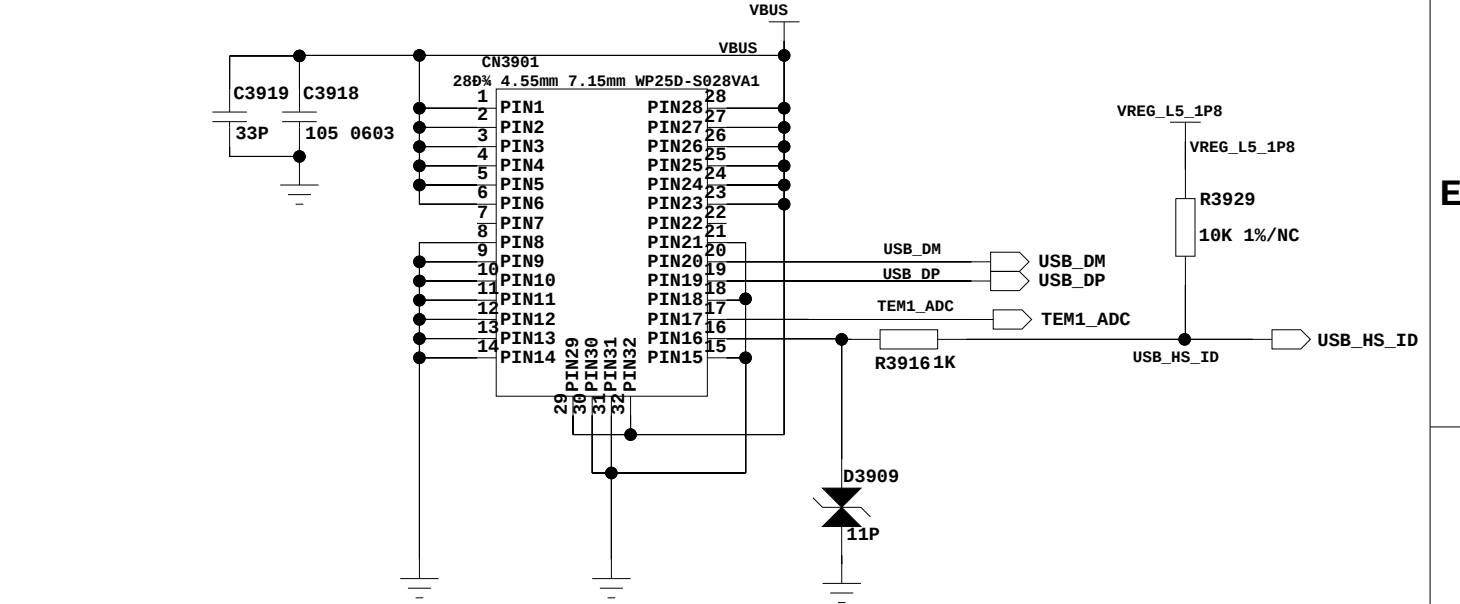
USBソケット



橋スライサIC



Fast charger CONN



	Sign	Date	Type:
Drawn By			Draw No: 15011
Checked By			Sheet of 30 Ver:
Standard By			oppo
Approved By			

